# Meta-F\*: Proof Automation with SMT, Tactics, and Metaprograms

Guido Martínez<sup>1,2</sup>, Danel Ahman<sup>3</sup>, Victor Dumitrescu<sup>4</sup>, Nick Giannarakis<sup>5</sup>, Chris Hawblitzel<sup>6</sup>, Cătălin Hriţcu<sup>2</sup>, Monal Narasimhamurthy<sup>7</sup>, Zoe Paraskevopoulou<sup>5</sup>, Clément Pit-Claudel<sup>8</sup>, Jonathan Protzenko<sup>6</sup>, Tahina Ramananadro<sup>6</sup>, Aseem Rastogi<sup>6</sup>, and Nikhil Swamy<sup>6</sup>

 $^1{\rm CIFASIS\text{-}CONICET}$   $^2{\rm Inria~Paris}$   $^3{\rm University~of~Ljubljana}$   $^4{\rm MSR\text{-}Inria~Joint~Centre}$   $^5{\rm Princeton~University}$   $^6{\rm Microsoft~Research}$   $^7{\rm University~of~Colorado~Boulder}$   $^8{\rm MIT~CSAIL}$ 

**Abstract.** We introduce Meta- $F^*$ , a tactics and metaprogramming framework for the  $F^*$  program verifier. The main novelty of Meta- $F^*$  is allowing the use of tactics and metaprogramming to discharge assertions not solvable by SMT, or to just simplify them into well-behaved SMT fragments. Plus, Meta- $F^*$  can be used to generate verified code automatically. Meta- $F^*$  is implemented as an  $F^*$  effect, which, given the powerful effect system of  $F^*$ , heavily increases code reuse and even enables the lightweight verification of metaprograms. Metaprograms can be either interpreted, or compiled to efficient native code that can be dynamically loaded into the  $F^*$  type-checker and can interoperate with interpreted code. Evaluation on realistic case studies shows that Meta- $F^*$  provides substantial gains in proof development, efficiency, and robustness.

### 1 Introduction

Scripting proofs using tactics and metaprogramming has a long tradition in interactive theorem provers (ITPs), starting with Milner's Edinburgh LCF [38]. In this lineage, properties of *pure* programs are specified in expressive higher-order (and often dependently typed) logics, and proofs are conducted using various imperative programming languages, starting originally with ML.

Along a different axis, program verifiers like Dafny [48], VCC [25], Why3 [34], and Liquid Haskell [60] target both pure and effectful programs, with side-effects ranging from divergence to concurrency, but provide relatively weak logics in which to specify properties (e.g., first-order logic with a few selected theories like linear arithmetic). They work primarily by computing verification conditions (VCs) from programs, usually relying on annotations such as pre- and postconditions, and encoding them to automated theorem provers (ATPs) such as SMT solvers, often providing excellent automation.

These two sub-fields have influenced one another, though the situation is somewhat asymmetric. On the one hand, most interactive provers have gained support for exploiting SMT solvers or other ATPs, providing push-button automation for certain kinds of assertions [28, 32, 44, 45, 55]. On the other hand,

recognizing the importance of interactive proofs, Why3 [34] interfaces with ITPs like Coq. However, working over proof obligations translated from Why3 requires users to be familiar not only with both these systems, but also with the specifics of the translation. And beyond Why3 and the tools based on it [27], no other SMT-based program verifiers have full-fledged support for interactive proving, leading to several downsides:

Limits to expressiveness The expressiveness of program verifiers can be limited by the ATP used. When dealing with theories that are undecidable and difficult to automate (e.g., non-linear arithmetic or separation logic), proofs in ATP-based systems may become impossible or, at best, extremely tedious.

**Boilerplate** To work around this lack of automation, programmers have to construct detailed proofs by hand, often repeating many tedious yet error-prone steps, so as to provide hints to the underlying solver to discover the proof. In contrast, ITPs with metaprogramming facilities excel at expressing domain-specific automation to complete such tedious proofs.

Implicit proof context In most program verifiers, the logical context of a proof is implicit in the program text and depends on the control flow and preand postconditions of preceding computations. Unlike in interactive proof assistants, programmers have no explicit access, neither visual nor programmatic, to this context, making proof structuring and exploration extremely difficult.

In direct response to these drawbacks, we seek a system that successfully combines the convenience of an automated program verifier for the common case, while seamlessly transitioning to an interactive proving experience for those parts of a proof that are hard to automate. Towards this end, we propose Meta- $F^*$ , a tactics and metaprogramming framework for the  $F^*$  [1, 59] program verifier.

## Highlights and Contributions of Meta-F\*

F\* has historically been more deeply rooted as an SMT-based program verifier. Until now, F\* discharged VCs exclusively by calling an SMT solver (usually Z3 [29]), providing good automation for many common program verification tasks, but also exhibiting the drawbacks discussed above.

Meta-F\* is a framework that allows F\* users to manipulate VCs using tactics. More generally, it supports metaprogramming, allowing programmers to script the construction of programs, by manipulating their syntax and customizing the way they are type-checked. This allows programmers to (1) implement custom procedures for manipulating VCs; (2) eliminate boilerplate in proofs and programs; and (3) to inspect the proof state visually and to manipulate it programmatically, addressing the drawbacks discussed above. SMT still plays a central role in Meta-F\*: a typical usage involves implementing tactics to transform VCs, so as to bring them into theories well-supported by SMT, without needing to (re)implement full decision procedures. Further, the generality of Meta-F\* allows

implementing non-trivial language extensions (e.g., typeclass resolution) entirely as metaprogramming libraries, without changes to the  $F^*$  type-checker.

The technical **contributions** of our work include the following:

"Meta-" is just an effect (§3.1) Meta- $F^*$  is implemented using  $F^*$ 's extensible effect system, which keeps programs and metaprograms properly isolated. Being first-class  $F^*$  programs, metaprograms are typed, call-by-value, direct-style, higher-order functional programs, much like the original ML. Further, metaprograms can be themselves verified (to a degree, see §3.4) and metaprogrammed.

Reconciling tactics with VC generation (§4.2) In program verifiers one often guides the solver towards the proof by supplying intermediate assertions. Meta-F\* retains this style, but additionally allows assertions to be solved by tactics. To this end, a contribution of our work is extracting from a VC a proof state encompassing all relevant hypotheses, even if implicit in the program text.

Executing metaprograms efficiently (§5) Metaprograms are executed during type-checking. As a baseline, they can be interpreted using  $F^*$ 's existing (but slow) abstract machine for term normalization, or a faster normalizer based on normalization by evaluation (NbE) [13, 19]. For much faster execution speed, metaprograms can be run natively. This is achieved combining the existing extraction mechanism of  $F^*$  to OCaml with a new framework for safely extending the  $F^*$  type-checker with such native code.

Examples (§2) and evaluation (§6) We evaluate Meta-F\* on several case studies. First, we present a functional correctness proof for the Poly1305 message authentication code (MAC) [14], using a novel combination of proofs by reflection for dealing with non-linear arithmetic and SMT solving for linear arithmetic. We measure a clear gain in proof robustness: SMT-only proofs succeed only rarely (for reasonable timeouts), whereas our tactic+SMT proof is concise, never fails, and is faster. Next, we demonstrate an improvement in expressiveness, by developing a small library for proofs of heap-manipulating programs in separation logic, which was previously out-of-scope for F\*. Finally, we illustrate the ability to automatically construct verified effectful programs, by introducing a library for metaprogramming verified low-level parsers and serializers with applications to network programming, where verification is accelerated by processing the VC with tactics, and by programmatically tweaking the SMT context.

We conclude that tactics and metaprogramming can be prosperously combined with VC generation and SMT solving to build verified programs with better, more scalable, and more robust automation.

# 2 Meta-F\* by Example

 $F^*$  is a general-purpose programming language aimed at program verification. It puts together the automation of an SMT-backed deductive verification tool

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with the expressive power of a language with full-spectrum dependent types. Briefly, it is a functional, higher-order, effectful, dependently typed language, with syntax loosely based on OCaml.  $F^*$  supports refinement types and Hoarestyle specifications, computing VCs of computations via a type-level weakest precondition (WP) calculus known as a *Dijkstra monad* [58].  $F^*$ 's effect system is also user-extensible [1]. Using it, one can model or embed imperative programming in styles ranging from ML to C [56] to assembly [36]. After verification,  $F^*$  programs can be extracted to efficient OCaml or F# code. A first-order fragment of  $F^*$ , called Low\*, can also be extracted to C via the KreMLin compiler [56].

This paper introduces Meta- $F^*$ , a metaprogramming framework for  $F^*$  that allows users to safely customize and extend  $F^*$  in many ways. For instance, Meta- $F^*$  can be used to preprocess or solve proof obligations; synthesize  $F^*$  expressions; generate top-level definitions; and resolve implicit arguments in user-defined ways, enabling non-trivial extensions. This paper primarily discusses the first two features. Technically, none of these features deeply increase the expressive power of  $F^*$ , since one could manually program in  $F^*$  terms that can now be metaprogrammed. However, as we will see shortly, manually programming terms and their proofs can be so prohibitively costly as to be practically infeasible.

Meta-F<sup>\*</sup> is similar to other tactic frameworks, such as Coq's [30] or Lean's [31], in presenting a set of goals to the programmer, providing commands to break them down, allowing to inspect and build abstract syntax, etc. In this paper, we mostly detail the characteristics where Meta-F<sup>\*</sup> differs from other engines.

This section presents Meta- $F^*$  informally, displaying its usage through case studies. We present any necessary  $F^*$  background as needed.

# 2.1 Tactics for Individual Assertions and Partial Canonicalization

Non-linear arithmetic reasoning is crucially needed for the verification of optimized, low-level cryptographic primitives [21, 66], an important use case for  $F^*$  [16] and other verification frameworks, including those that rely on SMT solving alone (e.g., Dafny [48]) as well as those that rely exclusively on tactic-based proofs (e.g., FiatCrypto [33]). While both styles have demonstrated significant successes, we make a case for a middle ground, leveraging the SMT solver for the parts of a VC where it is effective, and using tactics only where it is not.

We focus on Poly1305 [14], a widely-used cryptographic MAC that computes a series of integer multiplications and additions modulo a large prime number  $p=2^{130}-5$ . Implementations of the Poly1305 multiplication and mod operations are carefully hand-optimized to represent 130-bit numbers in terms of smaller 32-bit or 64-bit registers, using clever tricks; proving their correctness requires reasoning about long sequences of additions and multiplications.

Previously: Guiding SMT solvers by manually applying lemmas Prior proofs of correctness of Poly1305 and other cryptographic primitives using SMT-based program verifiers, including F\* [66] and Dafny [21], use a combination of SMT automation and manual application of lemmas. On the plus side, SMT

solvers are excellent at linear arithmetic, so these proofs delegate all associativity-commutativity (AC) reasoning about addition to SMT. Non-linear arithmetic in SMT solvers, even just AC-rewriting and distributivity, are, however, inefficient and unreliable—so much so that the prior efforts above (and other works too [41, 42]) just turn off support for non-linear arithmetic in the solver, in order not to degrade verification performance across the board, due to poor interaction of theories. Instead, users need to explicitly invoke lemmas.<sup>1</sup>

For instance, here is a statement and proof of a lemma about Poly1305 in F\*. The property and its proof do not really matter; the lines marked "(\*argh!\*)" do. In this particular proof, working around the inability of the solver to reason effectively about non-linear arithmetic, the programmer has spelled out basic facts about distributivity of multiplication and addition, by calling the library lemma distributivity\_add\_right, in order to guide the solver towards the proof. (Below, p44 and p88 represent 2<sup>44</sup> and 2<sup>88</sup> respectively)

Even at this relatively small scale, needing to explicitly instantiate the distributivity lemma is verbose and error prone. Even worse, the user is blind while doing so: the program text does not display the current set of available facts nor the final goal. If the user makes a mistake in some lemma call, she might simply get a useless hint instead of an error; then, the lemma fails to verify usually without providing much insight on why.

Given enough time, the solver can sometimes find a proof without the additional hints, but this is usually rare and dependent on context, and almost never robust. In this particular example we find by varying Z3's random seed that, in an isolated setting, the lemma is proven automatically about 32% of the time. The numbers are much worse for more complex proofs, and where the context contains many facts, making this style quickly spiral out of control. For example, a proof of one of the main lemmas in Poly1305, poly\_multiply, requires 41 steps

<sup>&</sup>lt;sup>1</sup> Lemma (requires pre) (ensures post) is  $F^*$  notation for the type of a computation proving pre  $\Longrightarrow$  post—we omit pre when it is trivial. In  $F^*$ 's standard library, math lemmas are proved using SMT with little or no interactions between problematic theory combinations. These lemmas can then be explicitly invoked in larger contexts, and are deleted during extraction.

of rewriting for associativity-commutativity of multiplication, and distributivity of addition and multiplication—making the proof too long to show here.

**SMT** and tactics in Meta-F\* The listing below shows the statement and proof of poly\_multiply in Meta-F\*, of which the lemma above was previously only a small part. Again, the specific property proven is not particularly relevant to our discussion. But, this time, the proof contains just two steps.

```
let poly_multiply (n p r h r0 r1 h0 h1 h2 s1 d0 d1 d2 h1 h2 hh : int) : Lemma (requires p > 0 \wedge r1 \geq 0 \wedge n > 0 \wedge 4 * (n * n) == p + 5 \wedge r == r1 * n + r0 \wedge h == h2 * (n * n) + h1 * n + h0 \wedge s1 == r1 + (r1 / 4) \wedge r1 % 4 == 0 \wedge d0 == h0 * r0 + h1 * s1 \wedge d1 == h0 * r1 + h1 * r0 + h2 * s1 \wedge d2 == h2 * r0 \wedge hh == d2 * (n * n) + d1 * n + d0) (ensures (h * r) % p == hh % p) = let r14 = r1 / 4 in let h_r_expand = (h2 * (n * n) + h1 * n + h0) * ((r14 * 4) * n + r0) in let hh_expand = (h2 * r0) * (n * n) + (h0 * (r14 * 4) + h1 * r0 + h2 * (5 * r14)) * n + (h0 * r0 + h1 * (5 * r14)) in let b = (h2 * n + h1) * r14 in modulo_addition_lemma hh_expand p b; assert (h_r_expand == hh_expand + b * (n * n * 4 + (-5))) by (canon_semiring_int_cxr) (* Proof_of_this_step_by_Meta-F*_tactic_*)
```

First, we call a single lemma about modular addition from  $F^*$ 's standard library. Then, we assert an equality annotated with a tactic (assert..by). Instead of encoding the assertion as-is to the SMT solver, it is preprocessed by the canon\_semiring tactic. The tactic is presented with the asserted equality as its goal, in an environment containing not only all variables in scope but also hypotheses for the precondition of poly\_multiply and the postcondition of the modulo\_addition\_lemma call (otherwise, the assertion could not be proven). The tactic will then canonicalize the sides of the equality, but notably only "up to" linear arithmetic conversions. Rather than fully canonicalizing the terms, the tactic just rewrites them into a sum-of-products canonical form, leaving all the remaining work to the SMT solver, which can then easily and robustly discharge the goal using linear arithmetic only.

This tactic works over terms in the commutative semiring of integers (int\_csr) using proof-by-reflection [15, 23, 37, 39]. Internally, it is composed of a simpler, also proof-by-reflection based tactic canon\_monoid that works over monoids, which is then "stacked" on itself to build canon\_semiring. The basic idea of proof-by-reflection is to reduce most of the proof burden to mechanical computation, obtaining much more efficient proofs compared to repeatedly applying lemmas. For canon\_monoid, we begin with a type for monoids, a small AST representing monoid values, and a denotation for expressions back into the monoid type.

```
type monoid (a:Type) = { unit : a; mult : (a \rightarrow a \rightarrow a); (* + monoid laws ... *) } type exp (a:Type) : Type = 
| Unit : exp a | Var : a \rightarrow exp a | Mult : exp a \rightarrow exp a \rightarrow exp a (* Note on syntax: #a below denotes that a is an implicit argument *)
```

```
\label{eq:approx} \begin{array}{l} \text{let rec denote } (\#a\text{:Type}) \text{ } (m\text{:monoid a}) \text{ } (e\text{:exp a}) \text{ : a} = \\ \text{match e with} \\ | \text{Unit } \rightarrow \text{m.unit } | \text{Var } x \rightarrow x \mid \text{Mult } x \text{ } y \rightarrow \text{m.mult } (\text{denote m } x) \text{ } (\text{denote m } y) \end{array}
```

To canonicalize an exp, it is first converted to a list of operands (flatten) and then reflected back to the monoid (mldenote). The process is proven correct, in the particular case of equalities, by the monoid reflect lemma.

```
 \begin{array}{l} \text{val flatten}: \#a\text{:}\mathsf{Type} \to \mathsf{exp} \ a \to \mathsf{list} \ a \\ \text{val mldenote}: \#a\text{:}\mathsf{Type} \to \mathsf{monoid} \ a \to \mathsf{list} \ a \to a \\ \text{let monoid\_reflect} \ (\#a\text{:}\mathsf{Type}) \ (\mathsf{m:monoid} \ a) \ (e_1 \ e_2 : \mathsf{exp} \ a) \\ & : \mathsf{Lemma} \ (\mathsf{requires} \ (\mathsf{mldenote} \ \mathsf{m} \ (\mathsf{flatten} \ e_1) == \ \mathsf{mldenote} \ \mathsf{m} \ (\mathsf{flatten} \ e_2))) \\ & \qquad \qquad (\mathsf{ensures} \ (\mathsf{denote} \ \mathsf{m} \ e_1 == \ \mathsf{denote} \ \mathsf{m} \ e_2)) = \ldots \\ \end{array}
```

At this stage, if the goal is  $t_1 == t_2$ , we require two monoidal expressions  $e_1$  and  $e_2$  such that  $t_1 == \text{denote m } e_1$  and  $t_2 == \text{denote m } e_2$ . They are constructed by the tactic canon\_monoid by inspecting the *syntax* of the goal, using Meta-F\*'s reflection capabilities (detailed ahead in §3.3). We have no way to prove once and for all that the expressions built by canon\_monoid correctly denote the terms, but this fact can be proven automatically at each application of the tactic, by simple unification. The tactic then applies the lemma monoid\_reflect m  $e_1e_2$ , and the goal is changed to mldenote m (flatten  $e_1$ ) == mldenote m (flatten  $e_2$ ). Finally, by normalization, each side will be canonicalized by running flatten and mldenote.

The canon\_semiring tactic follows a similar approach, and is similar to existing reflective tactics for other proof assistants [12, 39], except that it only canonicalizes up to linear arithmetic, as explained above. The full VC for poly\_multiply contains many other facts, e.g., that p is non-zero so the division is well-defined and that the postcondition does indeed hold. These obligations remain in a "skeleton" VC that is also easily proven by Z3. This proof is much easier for the programmer to write and much more robust, as detailed ahead in §6.1. The proof of Poly1305's other main lemma, poly reduce, is also similarly well automated.

Tactic proofs without SMT Of course, one can verify poly\_multiply in Coq, following the same conceptual proof used in Meta-F\*, but relying on tactics only. Our proof (see Fig. 1 in the appendix) is 27 lines long, two of which involve the use of Coq's ring tactic (similar to our canon\_semiring tactic) and omega tactic for solving formulas in Presburger arithmetic. The remaining 25 lines of proof include steps to destruct the propositional structure of terms, rewrite by equalities, enriching the context to enable automatic modulo rewriting (Coq does not fully automatically recognize equality modulo p as an equivalence relation compatible with the arithmetic operators). While a mature proof assistant like Coq has libraries and tools to make this kind of proof manipulation palatable, it can still be verbose.

In contrast, in Meta-F\* all of these mundane parts of a proof are quickly dispatched to an SMT solver, which decides linear arithmetic efficiently, beyond the quantifier-free Presburger fragment supported by tactics like omega, handles congruence closure natively, etc.

### 2.2 Tactics for Entire VCs and Separation Logic

A different way to invoke Meta-F\* is over an entire VC. While the exact shape of VCs is hard to predict, users with some experience can write tactics that find and solve particular sub-assertions within a VC, or simply massage them into shapes better suited for the SMT solver. We illustrate the idea on proofs of heap-manipulating programs.

One verification method that has eluded  $F^{\star}$  until now is separation logic, the main reason being that the pervasive "frame rule" requires instantiating existentially quantified heap variables, a challenge for SMT solvers. Manually specifying frames would also be extremely tedious for users. With Meta- $F^{\star}$ , one can do better. We have written a (proof-of-concept) embedding of separation logic and a tactic (sl\_auto) that performs heap frame inference automatically.

The approach we follow consists of designing the WP specifications for primitive stateful actions so as to make their footprint syntactically evident. The tactic then descends through VCs until it finds an existential for heaps arising from the frame rule. Then, by solving an equality between heap expressions (which requires canonicalization, for which we use a variant of canon\_monoid targetting commutative monoids) the tactic finds the frames and instantiates the existentials. Notably, as opposed to other tactic frameworks for separation logic [7, 46, 50, 52], this is all our tactic does before dispatching to the SMT solver, which can now be effective over the instantiated VC.

We now provide some detail on the framework. Below, 'emp' represents the empty heap, ' $\bullet$ ' is the separating conjunction and ' $r \mapsto v$ ' is the heaplet with the single reference r set to value v. Our development distinguishes between a "heap" and its "memory" for technical reasons, but we will treat the two as equivalent here. Further, defined is a predicate discriminating valid heaps (as in [53]), i.e., those built from separating conjunctions of actually disjoint heaps.

We first define the type of WPs and show the WP for the frame rule:

```
let pre = memory \rightarrow prop (* predicate on initial heaps *) let post a = a \rightarrow memory \rightarrow prop (* predicate on result values and final heaps *) let wp a = post a \rightarrow pre (* transformer from postconditions to preconditions *) let frame_post (#a:Type) (p:post a) (m_0:memory) : post a = \lambda x \ m_1 \rightarrow defined (m_0 • m_1) \wedge p x (m_0 • m_1) let frame_wp (#a:Type) (wp:wp a) (post:post a) (m:memory) = \exists m_0 \ m_1. defined (m_0 • m_1) \wedge m == (m_0 • m_1) \wedge wp (frame_post post m_1) m_0
```

Intuitively, frame\_post p m<sub>0</sub> behaves as the postcondition p "framed" by m<sub>0</sub>, i.e., frame\_post p m<sub>0</sub> x m<sub>1</sub> holds when the two heaps m<sub>0</sub> and m<sub>1</sub> are disjoint and p holds over the result value x and the conjoined heaps. Then, frame\_wp wp takes a postcondition p and initial heap m, and requires that m can be split into disjoint subheaps m<sub>0</sub> (the footprint) and m<sub>1</sub> (the frame), such that the postcondition p, when properly framed, holds over the footprint.

In order to provide specifications for primitive actions we start in small-footprint style. For instance, below is the WP for reading a reference:

```
let read wp (#a:Type) (r:ref a) = \lambdapost m<sub>0</sub> \rightarrow \exists x. m<sub>0</sub> == r \mapsto x \land post x m_0
```

We then insert framing wrappers around such small-footprint WPs when exposing the corresponding stateful actions to the programmer, e.g.,

```
val (!) : \#a:Type \rightarrow r:ref \ a \rightarrow STATE \ a \ (\lambda \ p \ m \rightarrow frame \ wp \ (read \ wp \ r) \ p \ m)
```

To verify code written in such style, we annotate the corresponding programs to have their VCs processed by sl\_auto. For instance, for the swap function below, the tactic successfully finds the frames for the four occurrences of the frame rule and greatly reduces the solver's work. Even in this simple example, not performing such instantiation would cause the solver to fail.

```
\begin{split} & \text{let swap\_wp } (r_1 \ r_2 : \text{ref int}) = \\ & \lambda p \ m \to \exists x \ y. \ m == (r_1 \mapsto x \bullet r_2 \mapsto y) \land p \ () \ (r_1 \mapsto y \bullet r_2 \mapsto x) \end{split} & \text{let swap } (r_1 \ r_2 : \text{ref int}) : \text{ST unit } (\text{swap\_wp } r_1 \ r_2) \ \text{by } (\text{sl\_auto } ()) = \\ & \text{let } x = !r_1 \ \text{in let } y = !r_2 \ \text{in } r_1 := y; \ r_2 := x \end{split}
```

The sl\_auto tactic: (1) uses syntax inspection to unfold and traverse the goal until it reaches a frame\_wp—say, the one for !r<sub>2</sub>; (2) inspects frame\_wp's first explicit argument (here read\_wp r<sub>2</sub>) to compute the references the current command requires (here r<sub>2</sub>); (3) uses unification variables to build a memory expression describing the required framing of input memory (here r<sub>2</sub>  $\mapsto$  ?u<sub>1</sub>  $\bullet$  ?u<sub>2</sub>) and instantiates the existentials of frame\_wp with these unification variables; (4) builds a goal that equates this memory expression with frame\_wp's third argument (here r<sub>1</sub>  $\mapsto \times \bullet$  r<sub>2</sub>  $\mapsto$  y); and (5) uses a commutative monoids tactic (similar to §2.1) with the heap algebra (emp,  $\bullet$ ) to canonicalize the equality and sort the heaplets. Next, it can solve for the unification variables component-wise, instantiating ?u<sub>1</sub> to y and ?u<sub>2</sub> to r<sub>1</sub> $\mapsto \times$ , and then proceed to the next frame wp.

In general, once the frames are instantiated, the SMT solver can efficiently prove the remaining assertions. In particular, all obligations about heap definedness are left to the solver. Thus, with relatively little effort, Meta- $F^*$  brings a widely used yet previously out-of-scope program logic (i.e., separation logic) into  $F^*$ . To the best of our knowledge, the ability to script separation logic into an SMT-based program verifier, without any primitive support, is unique.

### 2.3 Metaprogramming Verified Low-level Parsers and Serializers

Above, we used Meta-F\* to manipulate VCs for existing code. Here, we focus instead on generating the verified code automatically. We loosely refer to the previous setting as using "tactics", and to this one as "metaprogramming". In most ITPs, tactics and metaprogramming are not distinguished; however in a verifier of effectful programs like  $F^*$ , where some proofs are not materialized at all (§4.1), proving VCs of existing terms is distinct from generating new terms.

Metaprogramming in  $F^*$  involves programmatically generating a (potentially effectful) term (e.g., by constructing its syntax and instructing  $F^*$  how to typecheck it) and processing any VCs that arise via tactics. When applicable (e.g., when working in a domain-specific language), metaprogramming verified code can substantially reduce, or even eliminate, the burden of manual proofs.

We illustrate this by automating the generation of parsers and serializers from a type definition. Of course, this is a routine task in many mainstream metaprogramming frameworks (e.g., Template Haskell, camlp4, etc). The novelty here is that we produce imperative parsers and serializers extracted to C, with proofs that they are memory safe, functionally correct, and mutually inverse. This section is slightly simplified, more detail can be found in §A.

We proceed in several stages. First, we program a library of pure, high-level parser and serializer combinators, proven to be (partial) mutual inverses of each other. A parser for a type t is represented as a function possibly returning a t along with the amount of bytes of input consumed. The type of serializers is indexed by a p:parser t and contains a refinement stating that the parser is an inverse of the serializer. A package consists of a parser and its associated serializer.

```
let parser t = \text{seq byte} \rightarrow \text{option } (t * \text{nat})
let serializer \#t (p:parser t) = f:(t \rightarrow seq byte){\forall(x:t). p (f x) == Some (x, length (f x))}
type package t = | Mk : p:parser t \rightarrow s:serializer p \rightarrow package t
```

Basic combinators in the library include constructs for parsing and serializing base values and pairs, with the following signatures:

```
p_u8 : parse u8  
s_u8 : serializer p_u8  
p_pair : parser t1 \rightarrow parser t2 \rightarrow parser (t1 * t2)  
s_pair : serializer p1 \rightarrow serializer p2 \rightarrow serializer (p \rightarrow pair p1 p2)
```

Next, we define a library of low-level combinators that parse from and serialize to mutable arrays of bytes; these are coded in the Low\* subset of  $F^*$  that can be extracted to efficient C code. We prove our low-level combinators correct with respect to their high-level counterparts, and hence also mutually inverse. The type parser\_impl (p:parser t) is the type of an imperative function that reads from an array of bytes and returns a t, as characterized by the specificational parser p. Conversely, serializer\_impl (s:serializer p) writes into an array of bytes, as characterized by the specificational serializer s.

Given such a library, we would like to build verified, mutually inverse, low-level parsers and serializers for specific data formats. The task is mechanical, yet overwhelmingly tedious by hand, with many auxiliary proof obligations of a predictable structure: this is a perfect metaprogramming task.

Deriving specifications from a type definition Consider the following  $F^*$  type, representing lists of 18 pairs of bytes.

```
type sample = nlist 18 \text{ (u8 * u8)}
```

The first component of our metaprogram is <code>gen\_specs</code>, which from a type definition generates parser and serializer specifications.

```
\mathsf{let}\;\mathsf{ps\_sample}:\mathsf{package}\;\mathsf{sample} = \_\;\mathsf{by}\;(\mathsf{gen\_specs}\;(\mathsf{`sample}))
```

The syntax \_ by  $\tau$  is the way to call Meta-F\* for code generation. Meta-F\* will run the metaprogram  $\tau$  and, if successful, replace the underscore by the result. In this case, the gen\_specs (`sample) reflects on the syntax of the sample type (§3.3) and produces the pair of specificational parser and serializer below:

```
Mk \ (p\_nlist \ 18 \ (p\_u8 \ `and\_then` \ p\_u8)) \ (s\_nlist \ 18 \ (s\_u8 \ `and\_then` \ s\_u8))
```

Deriving low-level implementations that match specifications From this pair of specifications, we can generate a Low\* implementation via gen parser impl:

```
let p_low : parser_impl ps_sample.p = _ by gen_parser_impl let s_low : serializer_impl ps_sample.s = _ by gen_serializer_impl
```

which is a metaprogram producing the following implementation:

```
parse_nlist_impl 18ul (parse_u8_impl `and_then` parse_u8_impl) serialize nlist impl 18ul (serialize u8 impl `and then` serialize u8 impl)
```

using low-level parser and serializer combinators. These combinators are proved memory-safe and functionally correct with respect to the specification combinators, once and for all within the library.

For simple types like the ones above, the generated code is fairly simple. However, for more complex types, using the combinator library comes with non-trivial proof obligations. For example, even for a simple enumeration, type color = Red | Green, the parser specification is as follows:

```
\begin{array}{c} \mathsf{parse\_synth} \ (\mathsf{parse\_bounded\_u8}\ 2) \\ (\lambda \ \mathsf{x2} \to \mathsf{mk\_if\_t}\ (\mathsf{x2} = 0\mathsf{uy})\ (\lambda \ \_ \to \mathsf{Red})\ (\lambda \ \_ \to \mathsf{Green})) \\ (\lambda \ \mathsf{x} \to \mathsf{match}\ \mathsf{x}\ \mathsf{with}\ |\ \mathsf{Green} \to 1\mathsf{uy}\ |\ \mathsf{Red} \to 0\mathsf{uy}) \end{array}
```

We represent Red with 0uy and Green with 1uy. The parser first parses a "bounded" byte, with only two values. The parse\_synth combinator then expects functions between the bounded byte and the datatype being parsed (color), which must be proved to be in bijection. This proof is conceptually easy, but for large enumerations nested deep within the structure of other types, this is cumbersome. Interestingly, the SMT solver isn't particularly efficient at doing this proof either when faced with a large context of facts; since the proof is inherently computational, a proof that destructs the inductive type into its cases and then normalizes is much more natural. With our metaprogram, we can produce the term and then discharge any proof obligations that arise with a tactic, automating the whole process efficiently. We also explore simply tweaking the SMT context, again by a tactic, with good results. A quantitative evaluation is provided in §6.2.

The generated parsers and serializers are correct by construction, memory-safe, and mutually inverse. On extraction, the combinators are inlined to yield  $Low^*$  code that can be translated by the KreMLin compiler to efficient C code.

# 3 The Design of Meta-F\*

Having seen some of the use cases for Meta-F\*, we now turn to its design. As usual in proof-assistants, Meta-F\* tactics work over a set of goals and apply primitive actions to them, possibly solving some goals and generating new goals in the process. We instead focus the most on the aspects where Meta-F\* differs from other tactic engines. We first describe how metaprograms are modelled as

an effect ( $\S 3.1$ ). We then describe the runtime model for Meta-F\* ( $\S 3.2$ ). We then detail some of Meta-F\*'s syntax inspection and building capabilities ( $\S 3.3$ ). Finally, the tactic effect is also paired with a WP calculus, which enables some (lightweight) verification of metaprograms ( $\S 3.4$ ).

### 3.1 An Effect for Metaprogramming

Meta-F\* tactics are, at their core, programs that transform the "proof state", i.e. the set of remaining goals to be solved. As in Lean [31] and Idris [24], we define a monad combining exceptions and stateful computations over a proof state, along with actions that can access internal components such as the type-checker. For this we first introduce abstract types for the proof state, goals, terms, environments, etc., together with some functions to access them, some of them shown below.

```
type proofstate val goals_of : proofstate \rightarrow list goal type goal val goal_env : goal \rightarrow env type term val goal_type : goal \rightarrow term type env val goal_solution : goal \rightarrow term
```

Based on this, we can readily define our metaprogramming monad: tac. The monad combines  $F^*$ 's existing effect for potential divergence (Div), with a monad for exceptions and stateful computations over a proofstate. The definition of tac, shown below, is straightforward and given in  $F^*$ 's standard library. Then, we use  $F^*$ 's effect extension capabilities (see [1] for details) to elevate the tac monad and its actions to an effect, which we dub TAC.

```
type error = exn * proofstate (* error and proofstate at the time of failure *) type result a = | Success : a \rightarrow proofstate \rightarrow result a | Failed : error \rightarrow result a let tac a = proofstate \rightarrow Div (result a) let t_return #a (x:a) = \lambdaps \rightarrow Success x ps let t_bind #a #b (m:tac a) (f:a \rightarrow tac b) : tac b = \lambdaps \rightarrow ... (* omitted, yet simple *) let get () : tac proofstate = \lambdaps \rightarrow Success ps ps let raise #a (e:exn) : tac a = \lambdaps \rightarrow Failed (e, ps) new_effect { TAC with repr = tac ; return = t_return ; bind = t_bind ; get = get ; raise = raise }
```

The new\_effect declaration introduces the TAC effect using tac as its representation. Until §3.4 we only use the derived form Tac a, where a is the metaprogram's result type and the pre- and postconditions are trivial. The computation type Tac a is distinct from its underlying monadic representation type tac a—users cannot directly access the proof state aside from primitive actions provided by our framework. The simplest actions stem from the tac monad definition: get: unit  $\rightarrow$  Tac proofstate returns the current proof state and raise: exn  $\rightarrow$  Tac  $\alpha$  fails with the given exception (we use greek letters  $\alpha$ ,  $\beta$ , ... to abbreviate universally quantified type variables). Failures can be handled using catch: (unit  $\rightarrow$  Tac  $\alpha$ )  $\rightarrow$  Tac (either exn  $\alpha$ ), which resets the state on failure, including that of unification metavariables. We emphasize two points here. First, there is no "set" action. This is to forbid metaprograms from arbitrarily replacing their proof state, which

would be unsound. Instead, the proof state can only be modified by primitive actions that are guaranteed to preserve soundness (see §4.1). Second, the argument to catch must be thunked, since in F\* impure un-suspended computations are evaluated before they are passed into functions.

The only aspect differentiating Tac from other user-defined effects is the existence of effect-specific primitive actions, which give access to the metaprogramming engine proper. We list here but a few:

```
val trivial : unit \rightarrow Tac unit val tc : term \rightarrow Tac term val dump : string \rightarrow Tac unit
```

All of these are given an interpretation internally by Meta-F\*. For instance, trivial calls into F\*'s logical simplifier to check whether the current goal is a trivial proposition and discharges it if so, failing otherwise. The tc primitive queries the type-checker to infer the type of a given term in the current environment (F\* types are simply terms, hence the codomain of tc is also term). This does not change the proof state; its only purpose is to return useful information to the calling metaprograms. Finally, dump outputs the current proof state to the user in a pretty-printed format, in support of user interaction.

Having introduced the Tac effect and some basic actions, writing metaprograms is as straightforward as writing any other  $F^*$  code. For instance, here are two metaprogram combinators. The first one repeatedly calls its argument until it fails, returning a list of all the successfully-returned values. The second one behaves similarly, but folds the results with some provided folding function.

```
\begin{array}{l} \text{let rec repeat } (\tau : \text{unit} \to \mathsf{Tac} \; \alpha) : \mathsf{Tac} \; (\mathsf{list} \; \alpha) = \\ & \mathsf{match} \; \mathsf{catch} \; \tau \; \mathsf{with} \\ & | \; \mathsf{Inl} \; \_ \to [] \\ & | \; \mathsf{Inr} \; \mathsf{x} \to \mathsf{x} : : \; \mathsf{repeat} \; \tau \\ \\ \mathsf{let} \; \mathsf{repeat} \; \; \mathsf{fold} \; \mathsf{f} \; \mathsf{e} \; \tau = \mathsf{fold} \; \; \mathsf{left} \; \mathsf{f} \; \mathsf{e} \; (\mathsf{repeat} \; \tau) \end{array}
```

These two small combinators illustrate a few key points of Meta-F\*. As for all other F\* effects, metaprograms are written in applicative style, without explicit return, bind, or lift of computations (which are inserted under the hood). This also works across different effects: repeat\_fold can seamlessly combine the pure fold\_left from F\*'s list library with a metaprogram like repeat. Metaprograms are also type- and effect-inferred: while repeat\_fold was not at all annotated, F\* infers the polymorphic type  $(\beta \rightarrow \alpha \rightarrow \beta) \rightarrow \beta \rightarrow (\text{unit} \rightarrow \text{Tac } \alpha) \rightarrow \text{Tac } \alpha$  for it.

It should be noted that, if lacking an effect extension feature, one could embed metaprograms simply via the (properly abstracted) tac monad instead of the Tac effect. It is just more convenient to use an effect, given we are working within an effectful program verifier already. In what follows, with the exception of §3.4 where we infer and prove specifications for metaprograms, there is little reliance on metaprograms being embedded as an effect; so, our ideas could be applied in other settings.

### 3.2 Executing Metaprograms

Running Meta-F\* metaprograms requires three steps. First, they are reified into their underlying tac representation, as state-passing functions [1]. (User code cannot reify Tac computations: only F\* is able to do so.) Second, the resulting term is applied to an initial proof state, and then evaluated according to F\*'s dynamic semantics, for instance using F\*'s existing normalizer. For intensive applications, such as proofs by reflection, we provide faster alternatives (§5). In order to perform this second step, the proof state must be embedded as a term, i.e., as abstract syntax. Here is where its abstraction pays off: since metaprograms cannot interact with a proof state except through our limited interface, it need not be deeply embedded as syntax. By simply wrapping the internal proofstate into a new kind of term, and making the primitives aware of this wrapping, we can readily run the metaprogram that safely carries its "alien" proof state.

The third step is interpreting the primitives. They are realized by functions of similar types implemented within the  $F^*$  type-checker, but over an internal tac monad and the concrete definitions for term, proofstate, etc. Hence, there is a translation involved on every call and return, switching between embedded representations and their concrete variants. Take dump, for example, with type string  $\to$  Tac unit. When interpreting a call to it, the interpreter must unembed the argument (which is an  $F^*$  term) into a concrete string to pass to the implementation of dump. The situation is symmetric for the return value of the call, which must be embedded as terms. Fortunately, for the initial and final proof states the embedding and unembedding is very cheap, as explained above.

### 3.3 Syntax Inspection, Generation, and Quotation

If metaprograms are to be reusable over different kinds of goals, they must be able to reflect on the goals they are invoked to solve. Like any metaprogramming system, Meta-F\* offers a way to inspect and construct the syntax of F\* terms. Our representation of terms as an inductive type, and the variants of quotations, are inspired by the ones in Idris [24] and Lean [31].

Inspecting syntax Internally,  $F^*$  uses a locally-nameless representation with explicit, delayed substitutions. Hiding this complexity, we expose a simplified view [63] of terms to metaprograms, shielding them from some of the internal bureaucracy. Below we present a few constructors from the term\_view type:

```
\begin{array}{lll} \text{val inspect}: \text{term} \rightarrow \text{Tac term\_view} & \text{type term\_view} = \\ & \text{val pack}: \text{term\_view} \rightarrow \text{term} & \text{lower term\_view} = \\ & \text{lower term\_view} & \text{lower term\_vi
```

The term\_view type provides the "one-level-deep" structure of a term: metaprograms must call inspect to reveal the structure of the term, one constructor at

a time. The view exposes three kinds of variables: bound variables,  $Tv_BVar$ ; named local variables  $Tv_Var$ ; and top-level fully qualified names,  $Tv_FVar$ . Bound variables and local variables are distinguished since the internal abstract syntax is locally nameless. For metaprogramming, it is usually simpler to use a fully-named representation, so we provide inspect and pack functions that open and close binders appropriately to maintain this invariant. Since opening binders requires freshness, inspect has effect  $Tac.^2$ 

As generating large pieces of syntax via the view easily becomes tedious, we provide some ways of *quoting* terms in Meta-F $^*$ :

**Static quotations** A static quotation `e is just a shorthand for statically calling the  $F^*$  parser to convert e into the abstract syntax of  $F^*$  terms above. For instance, `(1+2) is equivalent to the following,

```
{\tt pack} \; ({\tt Tv\_App}({\tt pack} \; ({\tt Tv\_FVar} \; {\tt "Prims.op\_Addition"}), \; [{\tt pack} \; ({\tt Tv\_Const} \; ({\tt C\_Int} \; 1), \; ...])) \\
```

**Dynamic quotations** A second form of quotation is dquote: #a:Type  $\rightarrow$  a  $\rightarrow$  Tac term, an effectful operation that is interpreted by F\*'s normalizer during metaprogram evaluation. It returns the syntax of its argument at the time dquote e is evaluated. Evaluating dquote e substitutes all the free variables in e with their current values in the execution environment, suspends further evaluation, and returns the abstract syntax of the resulting term. For instance, evaluating  $(\lambda x \rightarrow dquote (x + 1))$  16 produces the abstract syntax of 16 + 1.

**Anti-quotations** Static quotations are useful for building big chunks of syntax concisely, but they are of limited use if we cannot combine them with existing bits of syntax. Subterms of a quotation are allowed to "escape" and be substituted by arbitrary expressions. We use the syntax #t to denote an antiquoted t, where t must be an expression of type term in order for the quotation to be well-typed. For example, (1 + #e) creates syntax for an addition where one operand is the integer constant 1 and the other is the term represented by e.

**Unquotation** Finally, we provide an effectful operation, unquote: #a:Type  $\to$  t:term  $\to$  Tac a, which takes a term representation t and an expected type for it a (usually inferred from the context), and calls the F\* type-checker to check and elaborate the term representation into a well-typed term.

#### 3.4 Specifying and Verifying Metaprograms

Since we model metaprograms as a particular kind of effectful program within  $F^*$ , which is a program verifier, a natural question to ask is whether  $F^*$  can specify and verify metaprograms. The answer is "yes, to a degree".

To do so, we must use the WP calculus for the TAC effect: TAC-computations are given computation types of the form TAC a wp, where a is the computation's

<sup>&</sup>lt;sup>2</sup> We also provide functions <code>inspect\_In</code>, <code>pack\_In</code> which stay in a locally-nameless representation and are thus pure, total functions.

result type and wp is a weakest-precondition transformer of type tacwp  $a = proofstate \rightarrow (result\ a \rightarrow prop) \rightarrow prop$ . However, since WPs tend to not be very intuitive, we first define two variants of the TAC effect: TacH in "Hoare-style" with pre- and postconditions and Tac (which we have seen before), which only specifies the return type, but uses trivial pre- and postconditions. The requires and ensures keywords below simply aid readability of pre- and postconditions—they are identity functions.

```
effect TacH (a:Type) (pre : proofstate \rightarrow prop) (post : proofstate \rightarrow result a \rightarrow prop) = TAC a (\lambda ps post' \rightarrow pre ps \wedge (\forall r. post ps r \Longrightarrow post' r)) effect Tac (a:Type) = TacH a (requires (\lambda _ \rightarrow \top)) (ensures (\lambda _ \rightarrow \rightarrow))
```

Previously, we only showed the simple type for the raise primitive, namely  $exn \rightarrow Tac \alpha$ . In fact, in full detail and Hoare style, its type/specification is:

```
raise : e:exn\rightarrow TacH \alpha (requires (\lambda \_ \rightarrow \top)) (ensures (\lambda ps r \rightarrow r == Failed (e, ps)))
```

expressing that the primitive has no precondition, always fails with the provided exception, and does not modify the proof state. From the specifications of the primitives, and the automatically obtained Dijkstra monad,  $F^*$  can already prove interesting properties about metaprograms. We show a few simple examples.

The following metaprogram is accepted by  $F^*$  as it can conclude, from the type of raise, that the assertion is unreachable, and hence raise\_flow can have a trivial precondition (as Tac unit implies).

```
let raise flow () : Tac unit = raise SomeExn; assert \bot
```

For  $\operatorname{cur\_goal\_safe}$  below, F\* verifies that (given the precondition) the pattern match is exhaustive. The postcondition is also asserting that the metaprogram always succeeds without affecting the proof state, returning some unspecified goal. Calls to  $\operatorname{cur\_goal\_safe}$  must ensure that the goal list is not empty, or F\* will statically reject the code.

```
 \begin{array}{c} \mathsf{let}\;\mathsf{cur\_goal\_safe}\;():\mathsf{TacH}\;\mathsf{goal}\;(\mathsf{requires}\;(\lambda\;\mathsf{ps}\;\to\neg(\mathsf{goals\_of}\;\mathsf{ps}\;==\;[])))\\ \qquad \qquad (\mathsf{ensures}\;(\lambda\;\mathsf{ps}\;\mathsf{r}\;\to\exists\mathsf{g.}\;\mathsf{r}\;==\;\mathsf{Success}\;\mathsf{g}\;\mathsf{ps})) = \\ \mathsf{match}\;\mathsf{goals\_of}\;(\mathsf{get}\;())\;\mathsf{with}\;|\;\mathsf{g}\;::\;\_\to\mathsf{g} \\ \end{array}
```

Finally, the divide combinator below "splits" the goals of a proof state in two at a given index n, and focuses a different metaprogram on each. It includes a runtime check that the given n is non-negative, and raises an exception in the TAC effect otherwise. Afterwards, the call to the (pure) List.splitAt function requires that its first argument be statically known to be non-negative, a fact which  $F^*$  can easily prove given the specification for raise and from the effect definition, which defines the control flow.

```
let divide (n:int) (tl : unit \rightarrow Tac \alpha) (tr : unit \rightarrow Tac \beta) : Tac (\alpha*\beta) = if n < 0 then raise NegativeN; let gsl, gsr = List.splitAt n (goals ()) in ...
```

This enables a style of "lightweight" verification of metaprograms, where expressive invariants about their state and control-flow can be encoded. The programmer can exploit dynamic checks (n < 0) and exceptions (raise) or static ones (preconditions), or a mixture of both, as needed.

Due to type abstraction though, the specifications of most primitives cannot provide complete detail about their behavior, and deeper specifications (such as ensuring a tactic will correctly solve a goal) cannot currently be proven, nor even stated—to do so would require, at least, an internalization of the typing judgment of F\*. While this is an exciting possibility [6], we have for now only focused on verifying basic safety properties of metaprograms, which helps users detect errors early, and whose proofs the SMT can handle well. Although in principle, one can also write tactics to discharge the proof obligations of metaprograms.

# 4 Meta-F\*, Formally

We now describe the trust assumptions for Meta-F<sup>\*</sup> (§4.1) and then how we reconcile tactics within a program verifier, where the exact shape of VCs is not given, nor known a priori by the user (§4.2).

### 4.1 Correctness and Trusted Computing Base (TCB)

As in any proof assistant, tactics and metaprogramming would be rather useless if they allowed to "prove" invalid judgments—care must be taken to ensure soundness. We begin with a taste of the specifics of  $F^*$ 's static semantics, which influence the trust model for Meta- $F^*$ , and then provide more detail on the TCB.

**Proof irrelevance in F^\*** The following two rules for introducing and eliminating refinement types are key in  $F^*$ , as they form the basis of its proof irrelevance.

$$\begin{array}{ll} \text{T-Refine} & \text{V-Refine} \\ \underline{\Gamma \vdash e \; : \; t \quad \Gamma \models \phi[e/x]} \\ \hline \Gamma \vdash e \; : \; x {:} t \{ \phi \} & \underline{\Gamma \models \phi[e/x]} \\ \end{array}$$

The  $\vDash$  symbol represents F\*'s validity judgment [1] which, at a high-level, defines a proof-irrelevant, classical, higher-order logic. These validity hypotheses are usually collected by the type-checker, and then encoded to the SMT solver in bulk. Crucially, the irrelevance of validity is what permits efficient interaction with SMT solvers, since reconstructing F\* terms from SMT proofs is unneeded.

As evidenced in the rules, validity and typing are mutually recursive, and therefore Meta-F\* must also construct validity derivations. In the implementation, we model these validity goals as holes with a "squash" type [8, 54], where squash  $\phi = \_:$ unit $\{\phi\}$ , i.e., a refinement of unit. Concretely, we model  $\Gamma \vDash \phi$  as  $\Gamma \vdash ?$ u: squash  $\phi$  using a unification variable. Meta-F\* does not construct deep solutions to squashed goals: if they are proven valid, the variable ?u is simply solved by the unit value '()'. At any point, any such irrelevant goal can be sent to the SMT solver. Relevant goals, on the other hand, cannot be sent to SMT.

Scripting the typing judgment A consequence of validity proofs not being materialized is that type-checking is undecidable in F\*. For instance: does the unit value () solve the hole  $\Gamma \vdash ?u : squash \phi?$  Well, only if  $\phi$  holds—a condition which no type-checker can effectively decide. This implies that the type-checker cannot, in general, rely on proof terms to reconstruct a proof. Hence, the primitives are designed to provide access to the typing judgment of F\* directly, instead of building syntax for proof terms. One can think of F\*'s type-checker as implementing one particular algorithmic heuristic of the typing and validity judgments—a heuristic which happens to work well in practice. For convenience, this default type-checking heuristic is also available to metaprograms: this is in fact precisely what the exact primitive does. Having programmatic access to the typing judgment also provides the flexibility to tweak VC generation as needed, instead of leaving it to the default behavior of F<sup>\*</sup>. For instance, the refine intro primitive implements T-REFINE. When applied, it produces two new goals, including that the refinement actually holds. At that point, a metaprogram can run any arbitrary tactic on it, instead of letting the F\* type-checker collect the obligation and send it to the SMT solver in bulk with others.

**Trust** There are two common approaches for the correctness of tactic engines: (1) the *de Bruijn criterion* [9], which requires constructing full proofs (or proof terms) and checking them at the end, hence reducing trust to an independent proof-checker; and (2) the LCF style, which applies backwards reasoning while constructing validation functions at every step, reducing trust to primitive, forward-style implementations of the system's inference rules.

As we wish to make use of SMT solvers within  $F^*$ , the first approach is not easy. Reconstructing the proofs SMT solvers produce, if any, back into a proper derivation remains a significant challenge (even despite recent progress, e.g. [20, 32]). Further, the logical encoding from  $F^*$  to SMT, along with the solver itself, are already part of  $F^*$ 's TCB: shielding Meta- $F^*$  from them would not significantly increase safety of the combined system.

Instead, we roughly follow the LCF approach and implement  $F^*$ 's typing rules as the basic user-facing metaprogramming actions. However, instead of implementing the rules in forward-style and using them to validate (untrusted) backwards-style tactics, we implement them directly in backwards-style. That is, they run by breaking down goals into subgoals, instead of combining proven facts into new proven facts. Using LCF style makes the primitives part of the TCB. However, given the primitives are sound, any combination of them also is, and any user-provided metaprogram must be safe due to the abstraction imposed by the Tac effect, as discussed next.

Correct evolutions of the proof state For soundness, it is imperative that tactics do not arbitrarily drop goals from the proof state, and only discharge them when they are solved, or when they can be solved by other goals tracked in the proof state. For a concrete example, consider the following program:

```
let f: \mathsf{int} \to \mathsf{int} = \_ by (\mathsf{intro}\ (); \, \mathsf{exact}\ (`42))
```

Here, Meta-F\* will create an initial proof state with a single goal of the form  $[\emptyset \vdash ?u_1 : int \rightarrow int]$  and begin executing the metaprogram. When applying the intro primitive, the proof state transitions as shown below.

$$[\emptyset \vdash ?u_1 : \mathsf{int} \to \mathsf{int}] \leadsto [\mathsf{x} : \mathsf{int} \vdash ?u_2 : \mathsf{int}]$$

Here, a solution to the original goal has not yet been built, since it depends on the solution to the goal on the right hand side. When it is solved with, say, 42, we can solve our original goal with  $\lambda x \to 42$ . To formalize these dependencies, we say that a proof state  $\phi$  correctly evolves (via f) to  $\psi$ , denoted  $\phi \leq_f \psi$ , when there is a generic transformation f, called a validation, from solutions to all of  $\psi$ 's goals into correct solutions for  $\phi$ 's goals. When  $\phi$  has n goals and  $\psi$  has m goals, the validation f is a function from term into term. Validations may be composed, providing the transitivity of correct evolution, and if a proof state  $\phi$  correctly evolves (in any amount of steps) into a state with no more goals, then we have fully defined solutions to all of  $\phi$ 's goals. We emphasize that validations are not constructed explicitly during the execution of metaprograms. Instead we exploit unification metavariables to instantiate the solutions automatically.

Note that validations may construct solutions for more than one goal, i.e., their codomain is not a single term. This is required in Meta-F\*, where primitive steps may not only decompose goals into subgoals, but actually combine goals as well. Currently, the only primitive providing this behavior is join, which finds a maximal common prefix of the environment of two irrelevant goals, reverts the "extra" binders in both goals and builds their conjunction. Combining goals using join is especially useful for sending multiple goals to the SMT solver in a single call. When there are common obligations within two goals, joining them before calling the SMT solver can result in a significantly faster proof.

We check that every primitive action respects the  $\leq$  preorder. This relies on them modeling F\*'s typing rules. For example, and unsurprisingly, the following rule for typing abstractions is what justifies the intro primitive:

$$\frac{\Gamma\text{-Fun}}{\Gamma, x: t \vdash e: t'} \frac{\Gamma, x: t \vdash e: t'}{\Gamma \vdash \lambda(x: t).e : (x: t) \rightarrow t'}$$

Then, for the proof state evolution above, the validation function f is the (mathematical, meta-level) function taking a term of type int (the solution for  $2u_2$ ) and building syntax for its abstraction over x. Further, the intro primitive respects the correct-evolution preorder, by the very typing rule (T-Fun) from which it is defined. In this manner, every typing rule induces a syntax-building metaprogramming step. Our primitives come from this dual interpretation of typing rules, which ensures that logical consistency is preserved.

Since the  $\leq$  relation is a preorder, and every metaprogramming primitive we provide the user evolves the proof state according  $\leq$ , it is trivially the case that the final proof state returned by a (successful) computation is a correct evolution of the initial one. That means that when the metaprogram terminates, one has indeed broken down the proof obligation correctly, and is left with a (hopefully)

simpler set of obligations to fulfill. Finally, with the evolution of the proof state being always governed by the  $\leq$  preorder, Tac provides an interesting example of monotonic state [2].

#### 4.2 Extracting Individual Assertions

As discussed, the logical context of a goal processed by a tactic is not always syntactically evident in the program. And, as shown in the List.splitAt call in divide from  $\S 3.4$ , some obligations crucially depend on the control-flow of the program. Hence, the proof state must crucially include these assumptions if proving the assertion is to succeed. Below, we describe how Meta-F\* finds proper contexts in which to prove the assertions, including control-flow information. Notably, this process is defined over logical formulae and does not depend at all on F\*'s WP calculus or VC generator: we believe it should be applicable to any VC generator.

As seen in §2.1, the basic mechanism by which Meta-F\* attaches a tactic to a specific sub-goal is assert  $\phi$  by  $\tau$ . Our encoding of this expression is built similarly to F\*'s existing assert construct, which is simply sugar for a pure function \_assert of type  $\phi$ :prop  $\rightarrow$  Lemma (requires  $\phi$ ) (ensures  $\phi$ ), which essentially introduces a cut in the generated VC. That is, the term (assert  $\phi$ ; e) roughly produces the verification condition  $\phi \land (\phi \Longrightarrow VC_e)$ , requiring a proof of  $\phi$  at this point, and assuming  $\phi$  in the continuation. For Meta-F\*, we aim to keep this style while allowing asserted formulae to be decorated with user-provided tactics that are tasked with proving or pre-processing them. We do this in three steps.

First, we define the following "phantom" predicate:

```
let with tactic (\phi: \mathsf{prop}) (\tau: \mathsf{unit} \to \mathsf{Tac} \; \mathsf{unit}) = \phi
```

Here  $\phi$  `with\_tactic`  $\tau$  simply associates the tactic  $\tau$  with  $\phi$ , and is equivalent to  $\phi$  by its definition. Next, we implement the assert\_by\_tactic lemma, and desugar assert  $\phi$  by  $\tau$  into assert\_by\_tactic  $\phi$   $\tau$ . This lemma is trivially provable by F\*.

```
\begin{array}{l} \mathsf{let} \ \mathsf{assert\_by\_tactic} \ (\phi \ : \mathsf{prop}) \ (\tau \ : \mathsf{unit} \to \mathsf{Tac} \ \mathsf{unit}) \\ \qquad \qquad : \ \mathsf{Lemma} \ (\mathsf{requires} \ (\phi \ \ \mathsf{`with\_tactic`} \ \tau \,)) \ (\mathsf{ensures} \ \phi \,) = () \end{array}
```

Given this specification, the term (assert  $\phi$  by  $\tau$ ; e) roughly produces the verification condition  $\phi$  `with\_tactic`  $\tau \wedge (\phi \Longrightarrow VC_e)$ , with a tagged left sub-goal, and  $\phi$  as an hypothesis in the right one. Importantly, F\* keeps the with\_tactic marker uninterpreted until the VC needs to be discharged. At that point, it may contain several subformulae annotated with tactics. For example, suppose the VC is the one given in (0) below, in which we distinguish an ambient context of variables and hypotheses  $\Delta$  (initially empty when processing a VC):

```
(0) \ \varDelta \models \mathsf{X} \Longrightarrow (\forall \ (\mathsf{x} \text{:} \mathsf{t}). \ \mathsf{R} \ `\mathsf{with} \ \ \mathsf{tactic} ` \ \tau_1 \land (\mathsf{R} \Longrightarrow \mathsf{S}))
```

Finally, in order to run the  $\tau_1$  tactic on R, we first need to "split it out". To do so, we must include all logical information "visible" for  $\tau_1$ , i.e., the set of premises of the implications traversed and the binders introduced by quantifiers. In F\*, these two cases in fact coincide since implication is simply a (non-dependent) universal quantification. As for any program verifier, these hypotheses include

the control flow information, postconditions, and any other logical fact that is known to be valid at the program point where the corresponding assert R by  $\tau_1$  was called. All of them are collected into  $\Delta$  as we traverse the term. In this case, the VC for just the sub-goal for R is:

(1) 
$$\Delta$$
, :X, x:t  $\models$  R

Afterwards, we can remove this obligation from the original VC. We do so by replacing it with  $\top$ , leaving a "skeleton" VC with all remaining facts.

(2) 
$$\Delta \models X \Longrightarrow (\forall (x:t). \top \land (R \Longrightarrow S))$$

The validity of (1) and (2) implies that of (0). The process also recursively descends into R and S, in case there are more with\_tactic markers in them. Then, the "split out" tactics are run (e.g.,  $\tau_1$  on (1)) to break them down further or solve them, and all remaining goals, plus the skeleton, are dispatched to SMT.

Note that while the *obligation* to prove R, in (1), is preprocessed by the tactic  $\tau_1$ , the *assumption* R for the continuation of the code, in (2), is left as-is. This is crucial for tactics such as the canonicalizer from §2.1: if the skeleton VC (2) contained an assumption for the canonicalized equality it would not help the SMT solver show the uncanonicalized postcondition.

However, not all nodes marked with with \_tactic are proof obligations. Suppose X in the previous VC was given as (Y `with\_tactic` $\tau_2$ ). In this case, one certainly does not want to attempt to prove Y, since it is an hypothesis. While it would be *sound* to prove it and replace it by  $\top$ , it is useless at best, and usually irreparably affects the system. Consider asserting the tautology ( $\bot$ `with\_tactic` $\tau$ )  $\Longrightarrow \bot$ ; replacing it with a pair of goals for  $\bot$  and  $\top\Longrightarrow\bot$  renders it unprovable.

Hence,  $F^*$  splits such obligations only in strictly-positive positions. On all others,  $F^*$  simply drops the with\_tactic marker, e.g., by just unfolding the definition of with\_tactic. For regular uses of the assert..by construct, however, all occurrences are strictly-positive. It is only when (expert) users use the with\_tactic marker directly that the above discussion might become relevant.

Formally, the soundness of this whole approach is given by the following metatheorem of  $F^*$ , justifying the splitting out of a node into a separate formula, and by the correct evolution of the proof state detailed above. The proof of Theorem 1 is straightforward, and included in  $\S B$ . We expect an analogous property to hold in other verifiers as well (in particular, it holds for first-order logic).

**Theorem 1.** Let E be a context with  $\Gamma \vdash E : prop \Rightarrow prop$ , and  $\phi$  a squashed proposition such that  $\Gamma \vdash \phi : prop$ . Then the following holds:

$$\frac{\varGamma \vDash E[\top] \quad \varGamma, \gamma(E) \vDash \phi}{\varGamma \vDash E[\phi]}$$

where  $\gamma(E)$  is the set of binders E introduces. If E is strictly-positive, then the reverse implication holds as well.

# 5 Executing Metaprograms Efficiently

 $F^*$  provides three complementary mechanisms for running metaprograms. The first two,  $F^*$ 's call-by-name (CBN) interpreter and a (newly implemented) call-by-value (CBV) NbE-based evaluator, support strong reduction—henceforth we refer to these as "normalizers". In addition, we design and implement a new native plugin mechanism that allows both normalizers to interface with Meta- $F^*$  programs extracted to OCaml, reusing  $F^*$ 's existing extraction pipeline for this purpose. Below we provide a brief overview of the three mechanisms.

# 5.1 CBN and CBV Strong Reductions

As described in §3.1, metaprograms, once reified, are simply  $F^*$  terms of type proofstate  $\rightarrow \mathsf{Div}$  (result a). As such, they can be reduced using  $F^*$ 's existing computation machinery, a CBN interpreter for strong reductions based on the Krivine abstract machine (KAM) [26, 47]. Although complete and highly configurable,  $F^*$ 's KAM interpreter is slow, designed primarily for converting types during dependent type-checking and higher-order unification.

Shifting focus to long-running metaprograms, such as tactics for proofs by reflection, we implemented an NbE-based strong-reduction evaluator for  $F^*$  computations. The evaluator is implemented in  $F^*$  and extracted to OCaml (as is the rest of  $F^*$ ), thereby inheriting CBV from OCaml. It is similar to Boespflug et al.'s 2011 NbE-based strong-reduction for Coq, although we do not implement their low-level, OCaml-specific tag-elimination optimizations—nevertheless, it is already vastly more efficient than the KAM-based interpreter.

### 5.2 Native Plugins & Multi-language Interoperability

Since Meta-F\* programs are just F\* programs, they can also be extracted to OCaml and compiled to run at native speed. Having a native version means one can directly call the metaprogram from the type-checker, which is more efficient than interpreting them. However, compilation has an overhead, and it would not be convenient to compile every single invocation. Instead, Meta-F\* enables users to compile individual metaprograms (usually those which are expected to be computation-intensive, e.g. canon\_semiring), and dynamically link their native versions into F\*. The normalizers are extended with such native versions, which are invoked the same way as Meta-F\* primitives. Users can then compile metaprograms as desired, while still quickly scripting their higher-level logic in the interpreter.

This requires (for higher-order metaprograms) a form of multi-language interoperability, converting between representations of terms used in the normalizers and in native code. We designed a small multi-language calculus, with ML-style polymorphism, to model the interaction between normalizers and native plugins, with conversions between the term representations. We outline it in §C. Beyond the notable efficiency gains of running compiled code vs. interpreting it, native metaprograms also require fewer embeddings. Once compiled, metaprograms work over the internal, concrete types for proofstate, term, etc., instead of over their  $F^*$  representations (though still treating them abstractly). Hence, compiled metaprograms can call primitives without needing to embed their arguments or unembed their results. Further, they can call each other directly as well. Indeed, operationally there is little difference between a primitive and a compiled metaprogram used as a plugin.

Native plugins, however, are not a replacement for the normalizers, for several reasons. First, there is an overhead in compilation which might not be justified by the execution speed-up. Second, extraction to OCaml erases types and proofs. As a result, the  $F^*$  interface of the native plugins can only contain types that can also be expressed in OCaml, thereby excluding full-dependent types—internally, however, they can be dependently typed. Third, being OCaml programs, native plugins do not support reducing open terms, which is often required for dependent type-checking. However, when the programs treat their open arguments parametrically, relying on parametric polymorphism, the normalizers can pass such arguments as-is, thereby recovering open reductions in some cases. This allows us to use native datastructure implementations (e.g. List), which is much faster than using the normalizers, even for open terms. We discuss this briefly in §C.

# 6 Experimental evaluation

We now present an experimental evaluation of Meta-F\*. First, we provide benchmarks comparing our reflective canonicalizer from 2.1 to calling the SMT solver directly without any canonicalization. Then, we return to the parsers and serializers from §2.3 and show how, for VCs that arise, a domain-specific tactic is much more tractable than a SMT-only proof.

### 6.1 A Reflective Tactic for Partial Canonicalization

In 2.1, we have described the canon\_semiring tactic that rewrites semiring expressions into sums of products. Differently from existing reflective tactics, we do not canonicalize expressions fully. Instead, we turn them into a shape that is close enough to their normal form for the SMT solver to be able to conclude their equality. This approach pays off well in practice. The table below compares the success rates of proofs for the poly\_multiply lemma from 2.1. To test the robustness of each alternative, we run the tests 150 times while varying the SMT solver's random seed. For the smtix we ask the solver to prove the lemma without any help from tactics, where i represents the resource limit (rlimit) multiplier given to the solver. This rlimit is memory-allocation based and independent of the particular system or current load. For the interp and native lines we make use of the canon\_semiring tactic, running it using F\*'s KAM normalizer and as a compiled plugin respectively—both run with the rlimit multiplier set to 1.

For each setup, we display the success rate of verification and the average (wall) time in seconds together with the standard deviation. The total verification time is then split into SMT solving and tactic evaluation.

The experiments clearly demonstrate a significant increase in robustness: proofs exploiting canonicalization have not been observed to fail, even with consistently lower rlimits. We recall that the tactic variants still leave goals

	Rate	Total	SMT	Meta-F*
smt1x	0%	$3.966 \pm 0.037$	$3.484 \pm 0.034$	
smt2x	1%	$4.287 \pm 0.046$	$3.805 \pm 0.044$	
smt3x	7%	$4.582 \pm 0.065$	$4.101 \pm 0.063$	
smt6x	10%	$6.043 \pm 0.161$	$5.489 \pm 0.157$	
smt12x	24%	$7.154 \pm 0.252$	$6.641 \pm 0.250$	
smt24x	40%	$10.144\pm0.647$	$9.649 \pm 0.647$	
smt48x	53%	$20.393\pm2.602$	$19.844\pm2.601$	
interp	100%	$3.973 \pm 0.051$	$2.008 \pm 0.027$	$1.440 \pm 0.020$
native	100%	$2.787 \pm 0.031$	$2.011\pm0.023$	$0.297\pm0.005$

for SMT solving, namely, the skeleton for the original VC and the canonicalized equality left by the tactic, easily dischargeable by the SMT solver through much more well-behaved linear reasoning. The last column shows that native compilation speeds up this tactic's execution by about 5x.

### 6.2 Combining SMT and Tactics for the Parser Generator

In §2.3, we presented a library of parser and serializer combinators and a metaprogramming approach to automate the construction of verified, mutually inverse, low-level parsers and formatter from a description of a given type. Metaprogramming helps inasmuch as it relieves the programmer from directly programming against our combinator library. Further, tactics are used to process and discharge proof obligations that arise when using our library.

We present three strategies for discharging auxiliary proofs, including proofs of bijectivity, that arise when constructing parsers and formatters for enumerated types. First, we used  $F^*$ 's default strategy to present all of these proofs directly to the SMT solver. Second, we programmed a  $\sim 100$  line tactic to discharge these proofs without relying on the SMT solver at all. Finally, we used a hybrid approach where a simple, 5-line tactic is used to prune the context of the proof removing redundant facts before presenting the resulting goals to the SMT solver.

The table alongside shows the total time in seconds for verifying metaprogrammed low-level parsers and formatters for enumerations of different sizes. In short, the hybrid approach scales the best;

$\# {\rm cases}$	SMT only	Tactic only	Hybrid
4	178	17.3	6.6
7	468	38.3	9.8
10	690	63.0	19.4

the tactic-only approach is somewhat slower; while the SMT-only approach scales poorly and is an order of magnitude slower. Our hybrid approach is very simple. With some more work, a more sophisticated hybrid strategy could be more performant still, relying on tactic-based normalization proofs for fragments of the VC best handled computationally (where the SMT solver spends most of its time), while using SMT only for integer arithmetic, congruence closure etc.

However, with Meta-F\*'s ability to manipulate proof contexts programmatically, our simple context-pruning tactic provides a big payoff at a small cost.

### 7 Related Work

Many SMT-based program verifiers [10, 11, 22, 35, 49], lacking a way to dynamically inspect the proof state, depend on user hints to succeed. This style of proving has been used in Dafny [3, 5, 48], Liquid Haskell [61, 62] and F\* itself [59]. In contrast, Why3 [34] allows VCs to be discharged using ITPs such as Coq, Isabelle/HOL, and PVS, but this requires using these external tools to discharged machine-generated proof goals after an additional embedding. In recent concurrent work, support for *effectful* reflection proofs was added to Why3 [51], and it would be interesting to investigate if this could also be done in Meta-F\*. Grov and Tumas [40] present Tacny, a tactic framework for Dafny, which is, however, limited in that it only transforms source code, with the program verifier unchanged. In contrast, Meta-F\* combines the benefits of an SMT-based program verifier and those of tactic proofs within a single language.

Moving away from SMT-based verifiers, ITPs have long relied on separate languages for proof scripting, starting with Edinburgh LCF [38] and ML, and continuing with HOL, Isabelle and Coq, which are either extensible via ML, or have dedicated tactic languages [6, 30, 57, 64]. Meta-F\* builds instead on a recent idea in the space of dependently typed ITPs [24, 31, 43, 65] of reusing the object-language as the meta-language. This idea first appeared in Mtac, a Cog-based tactics framework for Cog [43, 65], and has many generic benefits including reusing the standard library, IDE support, and type checker of the proof assistant. Mtac can additionally check the partial correctness of tactics, which is also sometimes possible in Meta-F\* but still rather limited (§3.4). Meta-F\*'s design is instead more closely inspired by the metaprogramming frameworks of Idris [24] and Lean [31], which provide a deep embedding of terms that metaprograms can inspect and construct at will without dependent types getting in the way. However, F\*'s effects, its weakest precondition calculus, and its use of SMT solvers distinguish Meta-F<sup>\*</sup> from these other frameworks, presenting both challenges and opportunities, as discussed in this paper.

Finally, ITPs are seeing increasing use of "hammers" such as Sledgehammer [17, 18, 55] in Isabelle/HOL, and similar tools for HOL Light and HOL4 [44], Mizar [45], and recently even Coq [28]. While hammers are similar to Meta-F\* tactics in as much they allow combining interactive and automated theorem proving, there are also significant differences. Hammers encode ITP goals to an automated prover, heuristically selecting the provided context, and, if a proof is found, attempt to reconstruct it internally in the ITP. Meta-F\* on the other hand uses F\*'s existing SMT encoding, which presents the (by default) full program context, and does not require to reconstruct a proof on success, since F\* natively supports SMT proofs. F\*'s SMT encoding is targeted at soundness and at efficient, scalable, and reproducible SMT solver behavior, while hammers often give up many of these and then regain them by proof reconstruction.

### 8 Conclusions

A key challenge in program verification is to balance tradeoffs among automation and expressiveness. Whereas tactic-based interactive provers support highly expressive logics, the tactic author is responsible for all the automation. Conversely, SMT-based program verifiers provide good, scalable automation for comparatively weaker logics, but offer little recourse when verification fails. A design that allows picking the right tool, at the granularity of each verification sub-task, is a worthy area that we feel has been underexplored. Meta-F\* aims to fill this gap: by using hand-written tactics along with SMT-automation, we have written proofs that were previously impractical in F\*, and (to the best of our knowledge) in other SMT-based program verifiers.

# A Metaprogramming verified parsers and serializers

We consider parser and serializer specifications as ghost code that "operates" on finite sequences of bytes of unbounded lengths. For a given type t, a serializer for t marshals any data of type t into a finite sequence of bytes; and a parser for t reads from a sequence of bytes, checks whether it corresponds to a valid data of type t, and if so, returns that parsed data and the number of bytes consumed.

The serializer specification always succeeds; the parser specification succeeds if and only if the input data is valid with respect to the parser. On those specifications, we aim to prove that the parser and the serializer are partial inverses of each other. We encode these requirements as refinements on the types of parser and serializer specifications:

```
type byte = FStar.UInt8.t  
type parser t = (input: seq byte) \rightarrow GTot (option (t * (x: nat { x < length input } )))  
let serialize_then_parse_eq_id #t (p: parser t) (s: (t \rightarrow GTot (seq byte))) =  
(\forall (x: t) . let y = s x in p y == Some (x, length y))  
let parse_then_serialize_eq_id #t (p: parser t) (s: (t \rightarrow GTot (seq byte))) =  
(\forall (x: seq byte) . match p x with | Some (y, len) \rightarrow s y == slice x 0 len | \_ \rightarrow \top) })  
type serialize_#t (p: parser t) = (s: (t \rightarrow GTot (seq byte))  
{ serialize_then_parse_eq_id p s \land parse_then_serialize_eq_id p s })
```

Whereas those specifications are ghost code, we would like to generate implementations that can be extracted to C. To this end, we generate *stateful* implementations written in the Low\* set of F\* [56], operating on buffers, which are Low\* mutable data structures representing C arrays. There, instead of a sequence of bytes, the parser implementation is given an input buffer and its length; and the serializer implementation is given an output buffer (along with its length) onto which it is to serialize the data. This means that, given a piece of data to serialize and a destination buffer, a serializer implementation can succeed only if the serialized data fits into the destination buffer; if so, then the serializer will return the number of bytes written.

We bake the correctness of the implementations with respect to their specifications at the level of their types:

```
type buffer8 = LowStar.Buffer.buffer FStar.UInt8.t type u32 = FStar.UInt32.t  
let parser32_postcond #t (p: parser t) (input: buffer8) (l: u32 {l == len input}) (h: mem) (res: option (t * u32)) (h': mem) = modifies loc_none h h' \land (* memory safety *) match p (as_seq h input), res with (* functional correctness *) | Some (x, ln), Some (x', ln') \rightarrow x' == x \land FStar.UInt32.v ln' == ln | None, None \rightarrow T| \rightarrow \bot  
type parser32 #t (p: parser t) = (input: buffer8) \rightarrow (l: u32 { l == len input } ) \rightarrow Stack (option (t * u32)) (requires (\lambda h \rightarrow live h input)) (ensures (parser32_postcond p input))
```

```
Lemma poly_multiply
       (n p r h r0 r1 h0 h1 h2 s1 d0 d1 d2 hh : Z)
       (H: p > 0 \land r1 \ge 0 \land n > 0 \land 4*(n*n) = p+5 \land r = r1*n+r0 \land r = r1*n+r0
                                         h = h2 * (n * n) + h1 * n + h0 \land s1 = r1 + (r1 / 4) \land r1 \mod 4 = 0 \land r1
                                          d0 = h0 * r0 + h1 * s1 \wedge d1 = h0 * r1 + h1 * r0 + h2 * s1 \wedge \\
                                         d2 = h2 * r0 \wedge hh = d2 * (n * n) + d1 * n + d0)
      : ((h * r) mod p = hh mod p).
Proof.
       repeat match goal with H : ?A \land ?B \mid => destruct H end.
       match goal with | ?A \mod p = ?B \mod p = > \text{change (eqm p A B) end.}
       match goal with K : r1 mod 4=0 \mid \_=>
         apply Z_div_exact_full<sub>2</sub> in K; try omega;
          revert K;
          generalize (r1 / 4);
         intro u;
         intros; subst
       end.
       pose (b := (h2 * n + h1) * u).
       generalize (Zopp eqm p).
       generalize (Zplus_eqm p).
       generalize (Zminus eqm p).
       generalize (Zmult eqm p).
       generalize (eqm setoid p).
       set (e := eqm p).
       intros.
       match goal with \mid e \_ ?R =>
             generalize (modulo addition lemma R p b)
       end.
       fold e. intro K. setoid rewrite < K. clear K.
       assert (p = 4 * (n * n) 5) as J by omega.
       replace (b * p) with (b * (4 * (n * n) 5)) by congruence.
       unfold b.
      apply eq_implies_eqm.
       ring.
Qed.
```

Fig. 1: Coq proof of poly\_multiply

```
let serializer32_postcond #t (p: parser t) (s: serializer p) (output: buffer8) (l: u32 {l == len output}) (x: t) (h: mem) (res: option u32) (h': mem) = live h output \land live h' output \land modifies (loc_buffer b) h h' \land (* memory safety *) let ln = length (s x) in match res with (* functional correctness *) | Some ln' \rightarrow FStar.UInt32.v ln' == ln \land ln \leq FStar.UInt32.v l \land let b' = sub b 0ul ln' in modifies (loc_buffer b') h h' \land as_seq h' b' == s x | None \rightarrow ln > FStar.UInt32.v l type serializer32 #t (#p: parser t) (s: serializer p) = (output: buffer8) \rightarrow (l: u32 { l == len output } ) \rightarrow (x: t) \rightarrow Stack (option u32) (requires (\lambda h \rightarrow live h output)) (ensures (serializer32_postcond s output l x))
```

Generating the specification from a  $F^*$  datatype Instead of mandating the user to write the parser and serializer specification themselves, we write a Meta- $F^*$  tactic, gen\_specs, to generate both the parser and the serializer specifications directly from the  $F^*$  type t given by the user. These tactics operate by syntax inspection on t itself, and generates the parser and serializer specifications using our combinators.

Contrary to Coq's Ltac, Meta-F\* tactics can also inspect definitions of inductive types. From there, we made <code>gen\_specs</code> also generate a parser and serializer specification out of an enumeration type defined as a F\* inductive type whose constructors have no arguments: for such a type with  $n \leq 256$  constructors, the corresponding parser will associate a unique 8-bit integer from 0 to n-1 to each constructor. For instance, if t is defined as a F\* enumeration type:

As before, by virtue of the correctness of serializers being baked in their type, the correctness of the serializer generated by <code>gen\_specs</code> does not appear as such as a verification condition when F\* is type-checking the generated term. In the example above, the only proof obligations generated are those to correctly type-check the rewriting functions themselves passed to <code>serialize\_synth</code> (e.g. enum constructors are rewritten to integers less than 4), and the precondition of <code>serialize\_synth</code> (i.e. the fact that the two rewriting functions are inverse of each other.) These can be discharged by the SMT solver, but they can also be discharged automatically by our tactics directly by case analysis on the inductive type, or by bounded integer enumeration.

**Binders** For instance, we specified and\_then, a combinator for dependent parsing:<sup>3</sup>

```
let and_then #t1 (p1: parser t1) #t2 (p2: (t1 \rightarrow Tot (parser t2))) : Tot (parser t2) = \lambdainput \rightarrow match p1 input with | None \rightarrow None | Some (x1, cons1) \rightarrow let input2 = slice input cons1 (length input) in match p2 x1 input2 with
```

<sup>&</sup>lt;sup>3</sup> This parsing combinator is based on yet unpublished work by Tej Chajed. Building serializers for parsers derived from and\_then is explicitly out of the scope of this paper.

```
| None \rightarrow None | Some (x2, cons2) \rightarrow Some (x2, cons1 + cons2)
```

Then, we implemented it in Low\* (parse\_and\_then\_impl), and manually proved it correct. From there, the user can write a simple parser specification for a parser that parses an integer encoded in one or two bytes depending on its value (where parse\_ret x is a parser that returns x without reading its byte input:)

```
let example : parser_spec FStar.Int16.t = and_then parse_u8  (\lambda \text{ lo} \rightarrow \text{if lo} < ^128 \text{uy then parse} \_\text{ret (cast} \_\text{u8}\_\text{to} \_\text{i16 lo}) \\ \text{else parse} \_\text{synth parse} \_\text{u8} \ (\lambda \text{ hi} \rightarrow \text{cast} \_\text{u8} \_\text{to} \_\text{i16 (lo} \%^{\ 128 \text{uy}}) \\ +^{\ \text{cast}} \ \text{u8} \ \text{to} \ \text{i16 hi}))
```

Then, if the user writes:

```
let example impl : parser impl example = by gen parser impl
```

then gen\_parser32 inspects the shape of the *goal*, which is parser32 example, and so generates the following Low\* implementation:

```
parse32 and then parse32 u8 (\lambda lo \rightarrow parse32 ifthenelse (lo <^ 128uy) (\lambda _ \rightarrow parse32 ret (cast u8 to i16 lo)) (\lambda _ \rightarrow parse32 synth parse32 u8 (\lambda hi \rightarrow cast u8 to i16 (lo \%^ 128uy) +^ cast u8 to i16 hi)))
```

from where KreMLin [56] then inlines the corresponding implementation combinators to produce C code.

First, gen\_parser32 inspects the shape of the goal to determine the type of the data to be parsed and the parser specification; then, it calls a recursive tactic gen\_parser32' that inspects the shape of the parser specification and actually builds the implementation.

```
module T = FStar.Tactics
let rec gen parser32' (env: T.env) (t: T.term) (p: T.term) : T.Tac T.term =
  let (hd, tl) = app head tail p in
  if hd `T.term eq` (`(parse ret)) then T.mk app (`(parse32 ret)) tl else
  if hd `T.term eq` (`(parse u8)) then (`(parse32 u8)) else
  if hd `T.term eq` (`(and then)) then match tl with
  \mid [(\mathsf{t}, \,\,\underline{\ }); \, (\mathsf{p}, \,\,\underline{\ }); \, (\mathsf{t}', \,\,\underline{\ }); \, (\mathsf{p}', \,\,\underline{\ })] \rightarrow
     begin match T.inspect p' with
     \mid T.Tv Abs bx body \rightarrow
       let p32 = gen parser32' env k t p in
        let \ env' = T.push \quad binder \ env \ bx \ in 
       let body' = gen parser32' env' k' t' body in
       let p32' = T.pack (T.Tv Abs bx body') in
       (`(parse32\_and\_then \#(`#t) \#(`#p) (`#p32) (`#t') (`#p') (`#p32')))
    end
  | \_ \rightarrow ...
  else
  ...
```

```
\label{eq:let_gen_parser32} \begin{tabular}{ll} \begin{tabular}{ll} let & (parser32) & (parser32) \\ let & (parser32) & (parser32) \\ let & (parser32) & (parser32) \\ let & (parser32) & (parser32) & (parser32) \\ let & (parser32) & (parser32) & (parser32) & (parser32) & (parser32) \\ let & (parser32) & (parser32) & (parser32) & (parser32) & (parser32) \\ let & (parser32) & (parser32)
```

When gen\_parser32 (resp. gen\_serializer32) builds the parser (resp. serializer) implementation, it may generate verification conditions due to specific preconditions required by certain combinators (such as the precondition of the serialize\_synth rewriting serializer combinator: the rewriting functions being inverse of each other). Contrary to Coq, there are no proof objects, so those preconditions need to be proven again even if they had been proven earlier by the user at the specification level. Nevertheless, those preconditions can be automatically solved by our tactics, or they can directly be sent to the SMT solver.

However, by virtue of the correctness of the implementation combinators being baked in their type, typechecking the resulting term generated by gen\_parser32 triggers no verification condition other than those preconditions required by the specific combinators, and unification constraints, the latter solved by reflexivity. In particular, the memory safety and correctness of the implementations generated by gen\_parser32 do not appear as such as verification conditions when F\* is type-checking the terms generated by gen\_parser32.

### A.1 Verified meta-programming of program transformations

compile\_bind expects, in bind f1 f2, that f2 be an abstraction, and so needs to manipulate binders and recursively compile f1 and the body of f2. This will, in the latter case, make some bound variables appear free in terms being compiled.

If the head term  $t_0$  is a free variable not corresponding to any combinator, then compile\_fvar unfolds it, compiles the unfolded term, and inserts a coercion. In most cases, a free variable to be unfolded will correspond to a function definition, so that unfolding will yield a  $\beta$ -redex, which compile\_fvar reduces by calling a normalization tactic that we provide as part of Meta-F\*. This needs an environment (e: env) tracking the bound variables encountered by nested calls to compile bind.

We show below the implementation of compile\_bind, which is representative of most features of Meta-F\* that we use for our compile tactic.

```
(* extract the head symbol and the arguments of an application *) let rec app_head_rev_tail (t: term) : Tac (term * list argv) =
```

We defined an explicit coercion combinator, coerce\_sz, such that coerce\_sz f1 f1\_sz f2 is well-typed and is a m\_sz f2 as soon as f1 () == f2 () as m specifications.

```
match inspect t with | Tv App u v \rightarrow let (x, I) = app head rev tail u in (x, v :: I) | \rightarrow
let app head tail (t: term): Tac (term * list argv) = let (x, l) = app head rev tail t in (x, rev l)
let compile bind (e: env) (ty: term) (t: term) (compile: env \rightarrow term \rightarrow term \rightarrow Tac term) : Tac term =
  let (t_0, ar) = app head tail t in
  guard (t_0 = quote bind); (* fail if false *)
  match ar with
  \mid [ (\tau_1, \_); (\tau_2, \_); (\mathsf{f1}, \_); (\mathsf{f2}, \_); ] \rightarrow
     begin match inspect f2 with
     | Tv Abs v f2 body \rightarrow
       let f1' = compile e \tau_1 f1 in
       let e' = push binder e v in
       let f2 body' = compile e' \tau_2 f2 body in
       let f2' = pack (Tv Abs v f2 body') in
       (`(bind sz #(`#\tau_1) #(`#\tau_2) (`#f1) (`#f1') (`#f2) (`#f2')))
     |\_ \rightarrow \mathsf{fail} \; \texttt{"compile\_bind:} \_ \mathsf{Not} \_ \mathsf{an} \_ \mathsf{abstraction"}
  \rightarrow fail "compile_bind:_4_arguments_expected"
```

Putting everything together Finally, the following wrapper combinator  $\Phi$  puts everything together, taking a specification f and its size and buffer implementations, and yielding a stateful function, fitting in the Low\* subset of F\* amenable to extraction to C, that first computes the expected size of the output, then allocates one single buffer using the C malloc, then uses it to write the output of f:

```
let \Phi \tau (f: m \tau) (f_sz: m_sz f) (f_st: m_st f): ST (option (\tau * buffer U8.t)) (requires (\lambda \to \to ) (ensures (\lambda h res h' \to let (r, log) = f () in if length log > 2^{31} - 1 then res == None else Some? res \wedge (let (Some (r', b)) = res in r == b \wedge fresh b h h' \wedge as_seq h b == log) )) = match f_sz with | None \to None | Some (_, sz') \to let b = rcreate_mm root 42uy sz in (* then allocate a fresh buffer *) let (r, _) = f_st b in (* then write the output into the buffer *) Some (r, b)
```

Related Work Our approach of generating verified low-level formatters is related to Amin and Rompf's [4] work on LMS-Verify. They use metaprogramming facilities in Scala to generate C code together with proof annotations to be checked by Frama-C [27], an SMT-based C verifier. In contrast, we generate correct-by-construction imperative Low\* code, which is verified by F\* before being translated to C by the KreMLin compiler. LMS-Verify has also been used to generate efficient and safe HTTP parsers, a larger-scale effort than the network message formatters than we have currently done.

### B Proof of Theorem 1

This proof is based on the formal definition of EMF\*, a recent formalization of an F\* subset [1]. We use the same notation and rule names from there, but the proof is nevertheless quite direct with just minimum familiarity with EMF\*. We use E to represent F\* contexts:

$$\begin{split} E &= \cdot \\ \mid tE & \mid Et \\ \mid \lambda(x:t).E \mid \lambda(x:E).t \\ \mid (x:t) \rightarrow E \mid (x:E) \rightarrow t \\ \mid x:t\{E\} & \mid x:E\{t\} \end{split}$$

Contexts present a set of bound variables to their hole. We denote those variables by  $\gamma(E)$ :

$$\gamma(\cdot) = \emptyset 
\gamma(\lambda(x:t).E) = \{x\} \cup \gamma(E) 
\gamma(\lambda(x:E).t) = \gamma(E) 
\dots$$

We say a context E has type  $t_1 \Rightarrow t_2$  for  $\Gamma$  (noted by  $\Gamma \vdash E : t_1 \Rightarrow t_2$ ) when for all e such that  $\Gamma, \gamma(E) \vdash e : t_1$  we also have  $\Gamma \vdash E[e] : t_2$ .

### B.1 Soundness of splitting the proof obligation

The proof follows mainly from the following lemma:

#### Lemma 1.

$$\frac{\Gamma, \gamma(E) \vDash t_1 = t_2}{\Gamma \vDash E[t_1] = E[t_2]}$$

*Proof.* The proof follows from applying functional extensionality and using a carefully crafted abstraction.

Let  $S = \lambda f.E[f(\gamma(E))]$  (where  $f(\gamma(E))$  represents f applied to every variable in  $\gamma(E)$ , sequentially). Also,  $\lambda \gamma(E).t$  represents abstracting E over the variables in  $\gamma(E)$ , and similarly for  $\forall \gamma(E).t$ .

Then,

$$\frac{\frac{\Gamma, \gamma(E) \vDash t_1 = t_2}{\Gamma \vDash \forall \gamma(E).t_1 = t_2}}{\frac{\Gamma \vDash (\lambda \gamma(E).t_1) = (\lambda \gamma(E).t_2)}{(\lambda \gamma(E).t_2)}} \overset{\text{V-Ext}}{\text{V-EqP}} \\ \frac{\Gamma \vDash S(\lambda \gamma(E).t_1) = S(\lambda \gamma(E).t_2)}{\Gamma \vDash E[t_1] = E[t_2]} \overset{\text{Reduction}}{\text{Reduction}}$$

Note that the particular set of contexts E does not influence the proof.

Having such lemma, the theorem is proven as follows:

$$\frac{\Gamma, \gamma(E) \vDash \phi}{\Gamma, \gamma(E) \vDash \phi \iff \top} \frac{\Gamma \text{rivial}}{\Pr \text{opExt}}$$

$$\frac{\Gamma \vDash E[\top] \qquad \Gamma \vDash E[\phi] = E[\top]}{\Gamma \vDash E[\phi]} \text{ Lemma}$$

$$\Gamma \vDash E[\phi]$$

### B.2 Partial completeness of splitting the proof obligation

While the previous theorem allows to soundly split any subformulae within a VC, we have seen that some of them constraint the system. Here we prove that for a particular set of contexts, splitting does not make the judgments stronger, and via Theorem 1, then they are equivalent.

We define a particular shape of "positive" contexts P. We don't attempt to follow all of EMF\*'s syntax, just the parts we commonly see as VCs in practice.

$$P = \cdot \\ \mid \phi \wedge P \mid P \wedge \phi \\ \mid \forall (x:t).P$$

In EMF\*, an implication  $a \Rightarrow b$  is just sugar for  $\forall (\underline{\ }: a).b$ , so they are considered as well.

**Theorem 2.** Take  $\Gamma$ , E, and  $\phi$  such that: (1)  $\phi$  is squashed, (2)  $\Gamma \vdash E : prop \rightarrow prop$  and (3)  $\Gamma \vdash \phi : prop$ . Then the following holds

$$\frac{\varGamma \vDash P[\phi]}{\varGamma, \gamma(P) \vDash \phi \qquad \varGamma \vDash P[\top]}$$

*Proof.* By induction on the structure of P, keeping  $\Gamma$  and  $\phi$  universally quantified.

- For  $P = \cdot$ , trivial
- For  $P = \psi \wedge P'$ , our hypothesis is  $\Gamma \vDash \psi \wedge P'[\phi]$ . Hence,  $\Gamma \vDash \psi$  and  $\Gamma \vDash P'[\phi]$ . By the IH, we get  $\Gamma, \gamma(P') \vDash \phi$  (note that  $\gamma(P) = \gamma(P')$ ) and  $\Gamma \vDash P'[\top]$ . By weakening and conjunction, we can prove  $\Gamma, \gamma(P') \vDash \psi \wedge \phi$  and  $\Gamma \vDash \psi \wedge P'[\top]$ , and conclude.
- For  $P = P' \wedge \psi$ , the reasoning is analogous.
- For  $P=\forall (x:t).P',$  our hypothesis is  $\Gamma \vDash \forall (x:t).P'[\phi].$  Hence, we get  $\Gamma, x:t \vDash P'[\phi]$  by eliminating the quantifier. From the IH, we get  $\Gamma, x:t, \gamma(P') \vDash \phi$  and  $\Gamma, x:t \vDash E[\top].$  From this last one, we can use V- $\forall$ i to obtain  $\Gamma \vDash \forall (x:t).E[\top],$  and conclude (noting that  $\gamma(P)=x:t, \gamma(P')$ ).

# C Modelling native plugins

This appendix contains the formal definitions of our multi-language interoperability model. We formalize the semantics of source and target language as well as the translation between the two. We convey the essential ideas in the text below, and present full details in Figures 4–18 that follow the discussion below.

# Part 1: Modeling Native Plugins with Simple Types

Reflecting our requirement that plugins are only supported at ML-typeable interfaces, we start with a source language that is an intrinsically typed (Church style), standard, simply typed lambda calculus with pairs and sums. Later in the section, we will add ML-style rank 1 polymorphism to it. Conversely, reflecting the type-less native representation of compiled OCaml code, our target language is the untyped lamdba calculus. For clarity, we markup the the syntax using colors, using blue for the source language and red for the target. Aside from the standard forms, we have two new expression forms to account for the "alien" expressions of one language appearing in the other:  $[e]_{\tau}$ , which embeds a  $\tau$ -typed source language expression into the target language, and  $\{e\}_{\tau}$ , which unembeds a target language expression to the source language at type  $\tau$ .

We explain the reductions through a small example. Consider a source language term  $(\lambda x:\mathbb{Z}.x)$  0, where we want to compile the identity function to native code and then perform the application. The first step is to translate the function to the target, by systematically erasing the types, and unembed it in the source. In our example, the source language term then becomes  $\{\lambda x.x\}_{\mathbb{Z}\to\mathbb{Z}}$  0.

Source semantics and unembedding (Figure 2) At a beta-reduction step (e.g. rule S-APP below), the source semantics uses a meta-function force(v) to examine the head of the application v. In addition to the usual values, the term  $\{v\}_{\tau}$  is a source value  $iff\ v$  is a target value. If the head is such a value, force(v) invokes the unembedding coercion  $\mathcal{C}_{\tau}(v)$  to coerce it to a suitable source value. For structural types, this amounts to lazily coercing the head constructor of the term (see below). For function types, a source-level closure is allocated, which first embeds its argument in the target, reduces a target application (using rule S-ALIEN), and then unembeds the result back in the source.

In our example, reduction proceeds by rule S-APP by first applying the unembedding coercion  $\mathcal{C}_{\mathbb{Z}\to\mathbb{Z}}()$  to  $\lambda x.x$  to get  $\lambda x:\mathbb{Z}.\{(\lambda x.x)\ [x]_{\mathbb{Z}}\}_{\mathbb{Z}}$ , and then applying the substitution to get  $\{(\lambda x.x)\ [0]_{\mathbb{Z}}\}_{\mathbb{Z}}$ .

Target semantics and embeddings (Figure 3) The target semantics is standard CBV. The only new rule is T-ALIEN, which first reduces the foreign source term using the source semantics, and then proceeds to *embed* the resulting value into the target language, using the embedding coercion  $\mathcal{C}_{\tau}(v)$ , as shown below. In our implementation, this rule is realized via a callback to the F\*'s normalizer.

$$\frac{e \ \rightarrow^* \ v}{\{e\}_{\tau} \ \rightarrow \ \{v\}_{\tau}} \text{ S-Alien} \qquad \qquad \frac{force(v) = \lambda x : \tau.e}{v \ e_2 \ \rightarrow \ e[e_2/x]} \text{ S-App}$$
 
$$force(v) = \begin{cases} \mathcal{C}_{\tau}(v) & \text{if } v = \{v\}_{\tau} \\ v & \text{otherwise} \end{cases}$$
 
$$\mathcal{C}_{\text{unit}}(()) = ()$$
 
$$\mathcal{C}_{\tau_1 \times \tau_2}((v_1, v_2)) = (\{v_1\}_{\tau_1}, \{v_2\}_{\tau_2})$$
 
$$\mathcal{C}_{\tau_1 \rightarrow \tau_2}(e) = \lambda x : \tau_1. \{e \ [x]_{\tau_1}\}_{\tau_2}$$

Fig. 2: Source semantics

$$\frac{e \to^* v}{[e]_{\tau} \to \mathcal{C}_{\tau}(v)} \text{ T-Alien} \qquad \frac{(\lambda x.e) \ v \to \ e[v/x]}{(\lambda x.e) \ v \to \ e[v/x]} \text{ T-App}$$

$$\mathcal{C}_{\tau_1 \to \tau_2}(e) = \lambda x.[e \ \{x\}_{\tau_1}]_{\tau_2}$$

$$\mathcal{C}_{\tau_1 \times \tau_2}((e_1,e_2)) = ([e_1]_{\tau_1},[e_2]_{\tau_2})$$

$$\mathcal{C}_{\tau'}(\{v\}_{\tau}) = v \text{ if } \tau \equiv \tau'$$

Fig. 3: Target semantics

The last case in the definition of the coercion cancels superfluous embeddings, in case the term inside is an unembedding. Continuing with our example,  $\{(\lambda x.x)\ [0]_{\mathbb{Z}}\}_{\mathbb{Z}}$  reduces using S-ALIEN, which reduces the application in its premise by first reducing  $[0]_{\mathbb{Z}}$  to 0 (using rule T-ALIEN, with  $\mathcal{C}_{\mathbb{Z}}(0)=0$ ), and then using rule T-APP to get 0. The rule S-ALIEN then returns  $\{0\}_{\mathbb{Z}}$  to the F\* normalizer, which applies the unembedding coercion to get 0.

### Part 2: Adding ML-style Polymorphism

The key observation in adding ML-style polymorphism is as follows. Since we only consider target terms compiled from well-typed source terms, by virtue of parametricity, a compiled polymorphic function must treat its argument abstractly. As a result, the normalizers can pass such arguments *as-is* without applying any embedding coercions—passing an open term is simply a subcase without further difficulty. Thus, we can leverage polymorphism to support such limited but useful open reductions.

In the formal model, we add an *opaque* construct to the target language, written as  $\langle e \rangle$ , which denotes an embedding of e at some abstract type A (as mentioned above, coercion to opaque is a no-op in the implementation). The coercion functions are extended with rules to introduce and eliminate opaque

terms (the  $\delta$  superscript carries type substitutions as a technical device), e.g.:

$$\mathcal{C}_A^{\delta}(e) = \langle e \rangle$$
  $\mathcal{C}_A^{\delta}(\langle e \rangle) = e$   $\mathcal{C}_{\tau_1 \times \tau_2}^{\delta}((e_1, e_2)) = (\mathcal{P}_{\tau_1}^{\delta}(e_1), \mathcal{P}_{\tau_2}^{\delta}(e_2))$ 

where  $\mathcal{P}_{\tau}^{\delta}(e)$  is  $\langle e \rangle$  when  $\tau = A$ , or a usual type-directed embedding otherwise. The source semantics is extended with a rule for type applications, while target semantics, being untyped, remains as-is.

Consider an application of the polymorphic identity function to a variable y:  $(AA.\lambda x:A.x) \mathbb{Z} y$ . As before, we would like to compile the function to native code and then reduce the applications—except this time it is an open term. Translation of the function and its embedding in the source yields  $\{\lambda x.x\}_{\forall A.A \to A}^{\top} \mathbb{Z} y$ . We now apply the unembedding coercion  $C_{\forall A.A \to A}(\lambda x.x)$  which returns a source type abstraction  $AA.\mathcal{C}_{A \to A}(\lambda x.x)$ . Reducing the arrow coercion next, we get  $AA.\lambda x:A.\{(\lambda x.x) \mathcal{P}_A(x)\}_A$ , where  $\mathcal{P}_A(x) = \langle x \rangle$ , as mentioned above. This is the key idea—when the original application reaches the beta-reduction with argument y (after the type application is reduced), y is simply substituted in this opaque construct, which is then returned back to the  $F^*$  normalizer as  $\{\langle y \rangle\}_A^{[A \to \mathbb{Z}]}$ . The normalizer can then apply the identity unembedding and get y.

This ability to reduce open terms relying on polymorphism allows us to evaluate expressions like  $\{\text{List.map}\}_{\forall A. \forall B. (A \to B) \to \text{list}A \to \text{list}B} \mathbb{Z} \mathbb{Z} \text{ f } [0; ...; 10^6], \text{ with f a free variable, using native datastructure implementations (List in this case), which is much faster than using the normalizers.$ 

# Part 3: Full Details on our Multi-language Interoperability Model

```
Type Schemes \sigma ::= unit |A| \tau_1 \rightarrow \tau_2 | \tau_1 \times \tau_2 | \tau_1 + \tau_2

Type Schemes \sigma ::= \forall A.\sigma | \tau

Environments \Gamma ::= \cdot |\Gamma, x : \tau
\Delta ::= \cdot |\Delta, A

Substitutions \delta ::= \cdot |\delta[A \mapsto \tau]

Expressions e ::= (0) |x| \lambda x : \tau . e | e_1 e_2
|\Lambda A.e| e \tau
|(e_1, e_2)| e.1 | e.2
| inl e| inr e
| case(e, x : \tau_1 . e_1, x : \tau_2 . e_2)
|\{e\}_{\tau}^{\delta}
```

Fig. 4: Source Language

```
Expressions e ::= () \mid x \mid \lambda x.e \mid e_1 e_2 \mid (e_1,e_2) \mid e.1 \mid e.2 \mid \text{inl } e \mid \text{inr } e \mid \text{case}(e,x.e_1,x.e_2) \mid [e]_{\tau}^{\delta} \mid \langle e \rangle
Environments \Gamma ::= \cdot \mid \Gamma, x
```

Fig. 5: Target Language

$$\Delta dash au$$
 wf

$$\frac{\Delta, A \vdash A \text{ wf}}{\Delta, A \vdash A \text{ wf}} \text{ TyVar} \qquad \frac{\Delta, A \vdash e \text{ wf}}{\Delta \vdash \forall A.\tau \text{ wf}} \text{ Scheme} \qquad \frac{\forall \ x, \ \Delta \vdash \Gamma(x) \text{ wf}}{\Delta \vdash \Gamma \text{ wf}} \text{ Env}$$

Fig. 6: Type Well-formedness

$$\Delta dash au$$
 wf

$$\frac{\Delta \vdash \delta \text{ wf} \quad \Delta \vdash \tau \text{ wf}}{\Delta, A \vdash \delta[A \mapsto \tau] \text{ wf}} \text{ Cons}$$

Fig. 7: Type Substitution Well-formedness

$$\Delta$$
;  $\Gamma \vdash e : \tau$ 

$$\frac{\varDelta;\ \Gamma \vdash e \ \mathrm{ok}}{\varDelta;\ \Gamma \vdash \{e\}_{\tau}^{\delta} : \delta\tau} \ \mathrm{Alien}$$

Fig. 8: Source Typing

$$e \rightarrow e'$$

$$\frac{e \to e'}{E[e] \to E[e']} \operatorname{CTX} \qquad \frac{force(v) = \lambda x : \tau.e}{v \ e_2 \to e[e_2/x]} \operatorname{App}$$
 
$$\frac{force(v) = \Lambda A.e'}{v \ \tau \to e'[\tau/A]} \operatorname{TApp} \qquad \frac{force(v) = \operatorname{in1} e'}{\operatorname{case}(v, x : \tau_1.e_1, x : \tau_2.e_2) \to e_1[e'/x]} \operatorname{CaseL}$$
 
$$\frac{force(v) = \operatorname{inr} e'}{\operatorname{case}(v, x : \tau_1.e_1, x : \tau_2.e_2) \to e_2[e'/x]} \operatorname{CaseR}$$
 
$$\frac{force(v) = (e_1, e_2)}{v.i \to e_i} \operatorname{Proj} \qquad \frac{e \to^* v}{\{e\}_\tau^\delta \to \{v\}_\tau^\delta} \operatorname{Alien}$$
 where 
$$force(v) = \begin{cases} \mathcal{C}_\tau^\delta(v) & \text{if } e = \{v\}_\tau^\delta \text{ and } e \to^* v \\ v & \text{otherwise} \end{cases}$$

Fig. 9: Evaluation in the Source Language

$$e \rightarrow e'$$

$$\frac{e \to e'}{E[e] \to E[e']} \operatorname{CTX} \qquad \overline{(\lambda x.e) \ v \to e[v/x]} \operatorname{App}$$

$$\overline{\operatorname{case}(\operatorname{inl} \ v, x.e_1, x.e_2) \to e_1[v/x]} \operatorname{CaseL}$$

$$\overline{\operatorname{case}(\operatorname{inr} \ v, x.e_1, x.e_2) \to e_2[v/x]} \operatorname{CaseL} \qquad \overline{(v_1, v_2).i \to v_i} \operatorname{Proj}$$

$$\frac{e \to^* v}{[e]_{\tau}^{\delta} \to \mathcal{C}_{\tau}^{\delta}(v)} \operatorname{Alien}$$

Fig. 10: Evaluation in the Target Language

$$()[\tau/A] = ()$$

$$\lambda x : \tau' \cdot e[\tau/A] = \lambda x : \tau'[\tau/A] \cdot e[\tau/A]$$

$$\vdots$$

$$\{e\}_{\tau'}^{\delta}[\tau/A] = \{e[\tau/A]\}_{\tau'}^{\delta[A \mapsto \tau]}$$

Fig. 11: Type Substitution in Source Terms

$$() [\tau/A] = ()$$

$$\lambda x.e[\tau/A] = \lambda x.e[\tau/A]$$

$$\vdots$$

$$[e]_{\tau'}^{\delta} [\tau/A] = [e[\tau/A]]_{\tau'}^{\delta[A \mapsto \tau]}$$

$$\langle v \rangle = \langle v[\tau/A] \rangle$$

Fig. 12: Type Substitution in Target Terms

$$\frac{e_{i} \rightsquigarrow e_{i}}{(e_{1}, e_{2}) \rightsquigarrow (e_{1}, e_{2})} \text{ Pair } \frac{e \rightsquigarrow e}{e.i \rightsquigarrow e.i} \text{ Proj } \frac{e \rightsquigarrow e}{\text{inl } e \rightsquigarrow \text{inl } e} \text{ Inl}$$

$$\frac{e \rightsquigarrow e}{\text{inr } e \rightsquigarrow \text{inr } e} \text{ Inr}$$

$$\frac{e \rightsquigarrow e}{\text{case}(e, x : \tau_{1}.e_{1}, x : \tau_{2}.e_{2})} \rightsquigarrow \text{case}(e, x.e_{1}, x.e_{2})$$

$$\frac{e[\{x\}_{\tau}^{\cdot}/x] \rightsquigarrow e}{\lambda x : \tau.e \rightsquigarrow \lambda x.e} \text{ Abs } \frac{e_{i} \rightsquigarrow e_{i}}{e_{1} e_{2} \rightsquigarrow e_{1} e_{2}} \text{ App } \frac{e \rightsquigarrow e}{\Lambda A.e \rightsquigarrow e} \text{ TAbs}$$

$$\frac{e \rightsquigarrow e}{e \tau \rightsquigarrow e} \text{ TApp}$$

Fig. 13: Translation

```
\mathcal{C}_{	ext{unit}}^{\delta}(()) = ()
\mathcal{C}_{	au_1 	o 	au_2}^{\delta}(\lambda x.e) = \lambda x : \delta 	au_1 \cdot \{e[\mathcal{P}_{	au_1}^{\delta}(x)/x]\}_{	au_2}^{\delta}
\mathcal{C}_{	au_1 	imes 	au_2}^{\delta}((v_1, v_2)) = (\{v_1\}_{	au_1}^{\delta}, \{v_2\}_{	au_2}^{\delta})
\mathcal{C}_{	au_1 + 	au_2}^{\delta}(	ext{inl } v) = 	ext{inl } \{v\}_{	au_1}^{\delta}
\mathcal{C}_{	au_1 + 	au_2}^{\delta}(	ext{inr } v) = 	ext{inr } \{v\}_{	au_2}^{\delta}
\mathcal{C}_{A}^{\delta}(\langle e \rangle) = e
\mathcal{C}_{A}^{\delta}(\langle e \rangle) = AA.\mathcal{C}_{\sigma}^{\delta}(v)
```

Fig. 14: Target to Source Coercion

$$egin{aligned} \mathcal{C}_{ ext{unit}}^{\delta}(()) &= () \ \mathcal{C}_{ au_1 
ightarrow au_2}^{\delta}(\lambda x : au. e) &= \lambda x. \mathcal{P}_{ au_2}^{\delta}(e[\{x\}_{ au_1}^{\delta}/x]) \ \mathcal{C}_{ au_1 imes au_2}^{\delta}((e_1, e_2)) &= (\mathcal{P}_{ au_1}^{\delta}(e_1), \mathcal{P}_{ au_2}^{\delta}(e_2)) \ \mathcal{C}_{ au_1 + au_2}^{\delta}( ext{inl } e) &= ext{inl } \mathcal{P}_{ au_1}^{\delta}(e) \ \mathcal{C}_{ au_1 + au_2}^{\delta}( ext{inr } e) &= ext{inr } \mathcal{P}_{ au_2}^{\delta}(e) \ \mathcal{C}_{A}^{\delta}(e) &= \langle e \rangle \ \mathcal{C}_{ au'}^{\delta}(\{e\}_{ au}^{\delta}) &= e \end{aligned} \qquad \text{if } au \equiv au'$$

Fig. 15: Source to Target Coercion

$$\mathcal{P}_{\tau}^{\delta}(e) = \begin{cases} \langle e \rangle & \text{if } \tau = A \\ [e]_{\tau}^{\delta} & \text{otherwise} \end{cases}$$

Fig. 16: Protect meta-function

$$\begin{array}{c} \text{unit} \equiv \text{unit} \stackrel{\mathsf{def}}{=} \top \\ A \equiv B \stackrel{\mathsf{def}}{=} \top \\ (\tau_1, \tau_2) \equiv (\tau_1', \tau_2') \stackrel{\mathsf{def}}{=} \tau_1 \equiv \tau_1' \wedge \tau_2 \equiv \tau_2' \\ \tau_1 {\rightarrow} \tau_2 \equiv \tau_1' {\rightarrow} \tau_2' \stackrel{\mathsf{def}}{=} \tau_1 \equiv \tau_1' \wedge \tau_2 \equiv \tau_2' \\ \tau_1 {+} \tau_2 \equiv \tau_1' {+} \tau_2' \stackrel{\mathsf{def}}{=} \tau_1 \equiv \tau_1' \wedge \tau_2 \equiv \tau_2' \\ \tau \equiv \tau' \stackrel{\mathsf{def}}{=} \bot & \text{otherwise} \end{array}$$

Fig. 17: Type equivalence

 $e \rightsquigarrow e$ 

$$\frac{e_{i} \leadsto e_{i}}{(e_{1},e_{2}) \leadsto (e_{1},e_{2})} \text{ PAIR } \qquad \frac{e \leadsto e}{e.i \leadsto e.i} \text{ PROJ } \qquad \frac{e \leadsto e}{\text{inl } e \leadsto \text{inl } e} \text{ INL}$$

$$\frac{e \leadsto e}{\text{inr } e \leadsto \text{inr } e} \text{ INR}$$

$$\frac{e \leadsto e}{\text{case}(e,x:\tau_{1}.e_{1},x:\tau_{2}.e_{2})} \leadsto \text{case}(e,x.e_{1},x.e_{2}) \xrightarrow{\text{CASE}}$$

$$\frac{e[\{x\}_{\tau}^{+}/x] \leadsto e}{\lambda x:\tau.e \leadsto \lambda x.e} \text{ Abs } \qquad \frac{e_{i} \leadsto e_{i}}{e_{1} e_{2} \leadsto e_{1} e_{2}} \text{ App } \qquad \frac{e \leadsto e}{\Lambda A.e \leadsto e} \text{ TAbs}$$

$$\frac{e \leadsto e}{e \tau \leadsto e} \text{ TApp}$$

Fig. 18: Translation

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